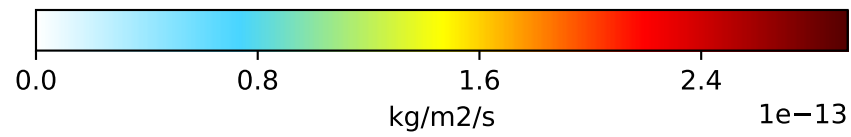
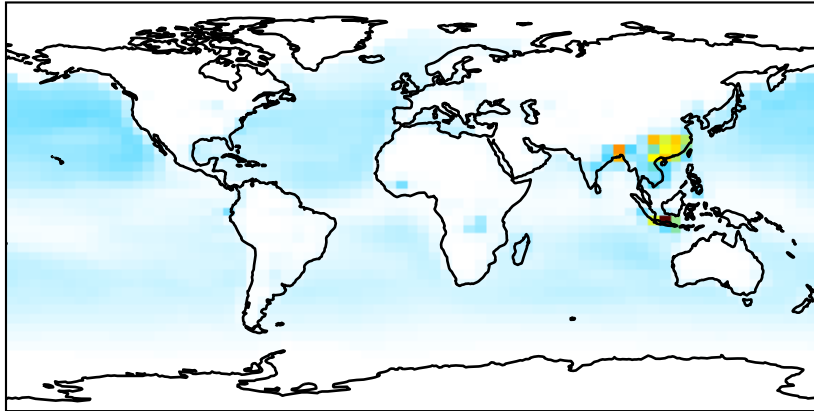
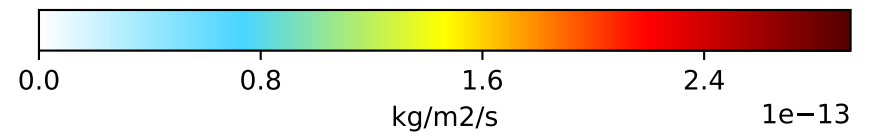
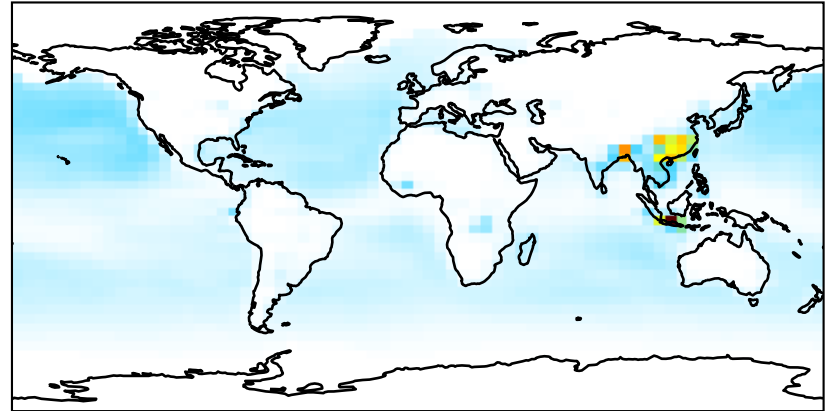


EmisCH3I_Ocean (Apr2019)

GCC_14.0.0 (Ref)
4.0x5.0



GCC_14.2.0 (Dev)
4.0x5.0



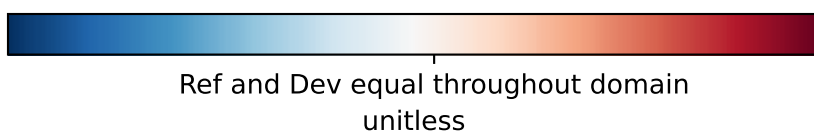
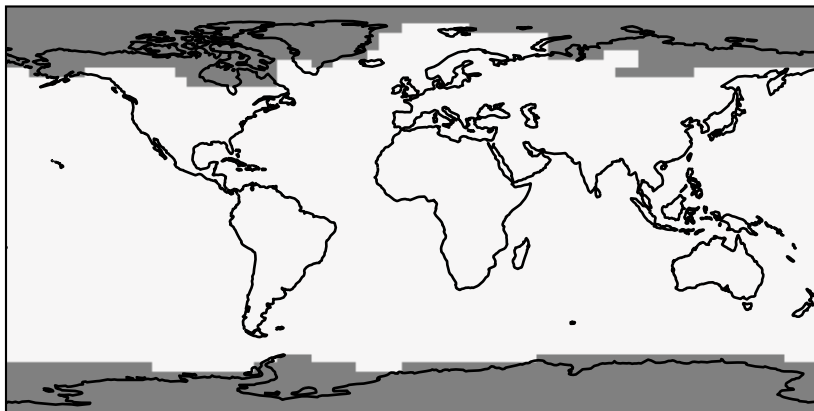
Difference
Dev - Ref, Dynamic Range



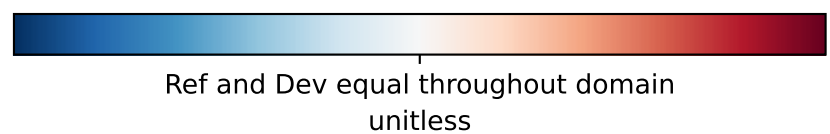
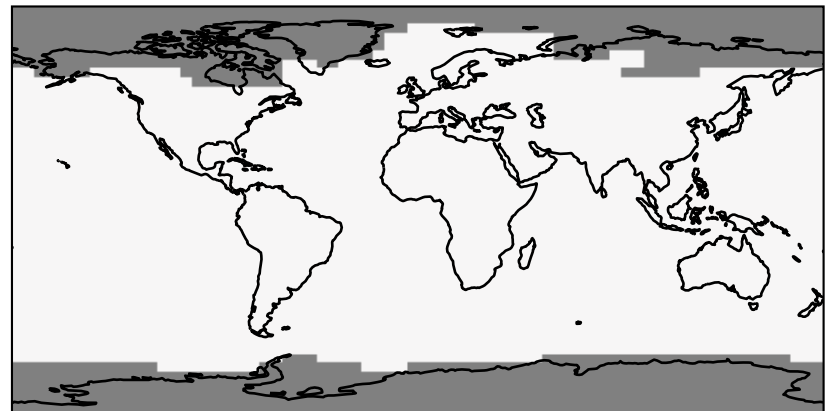
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

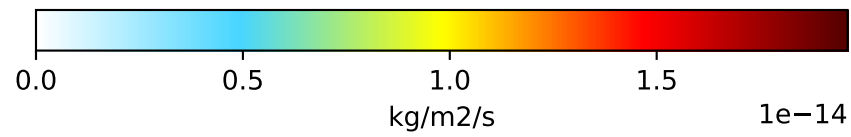
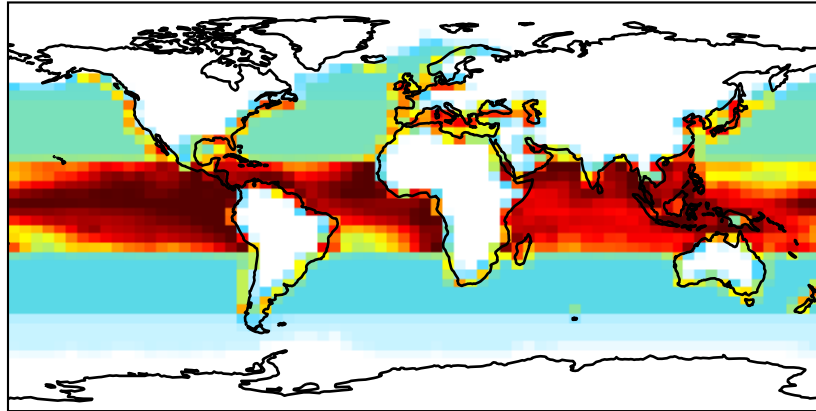


Ratio
Dev/Ref, Fixed Range

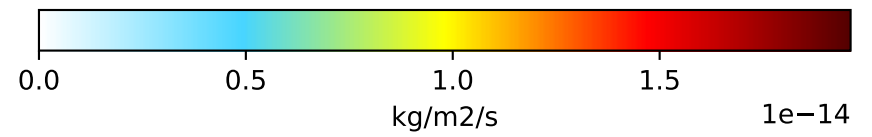
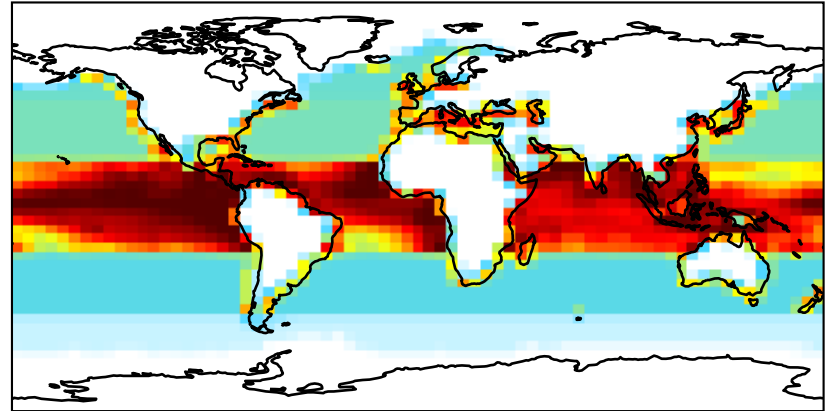


EmisCH2I2_Ocean (Apr2019)

GCC_14.0.0 (Ref)
4.0x5.0



GCC_14.2.0 (Dev)
4.0x5.0



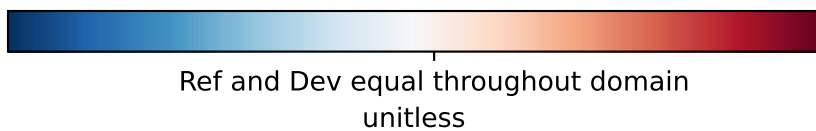
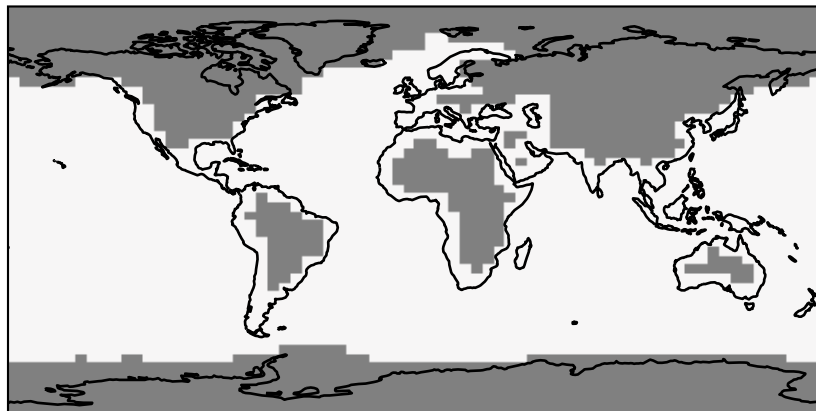
Difference
Dev - Ref, Dynamic Range



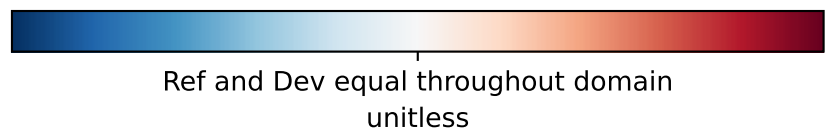
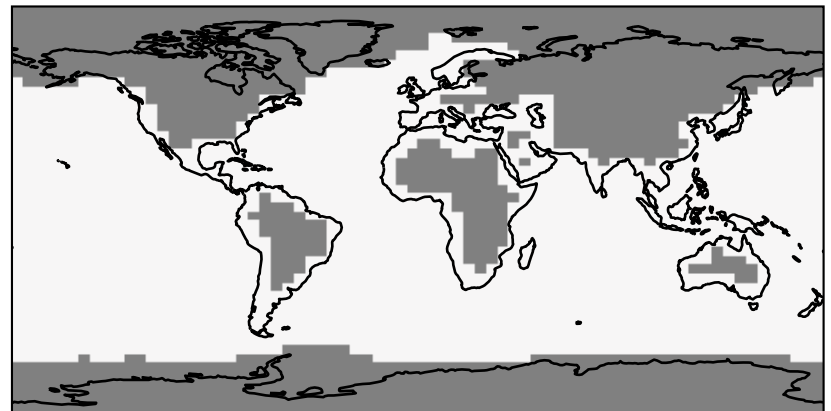
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

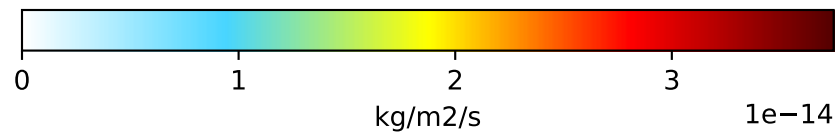
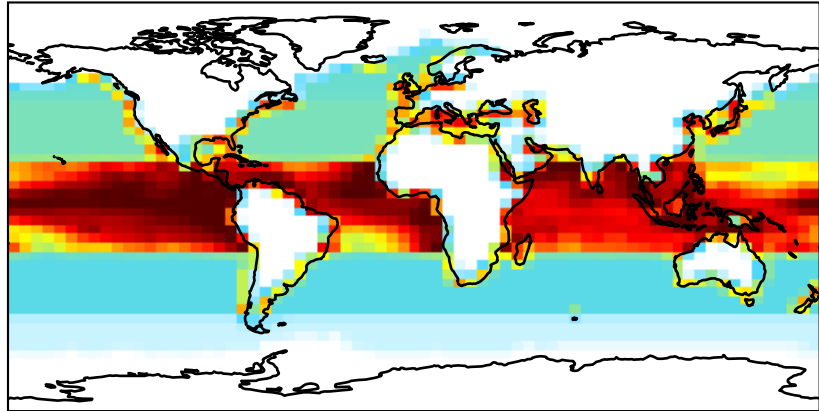


Ratio
Dev/Ref, Fixed Range

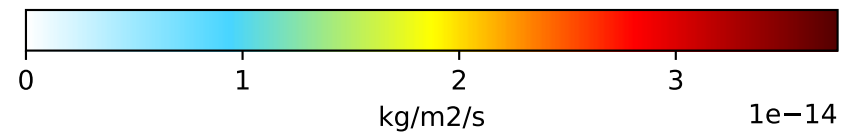
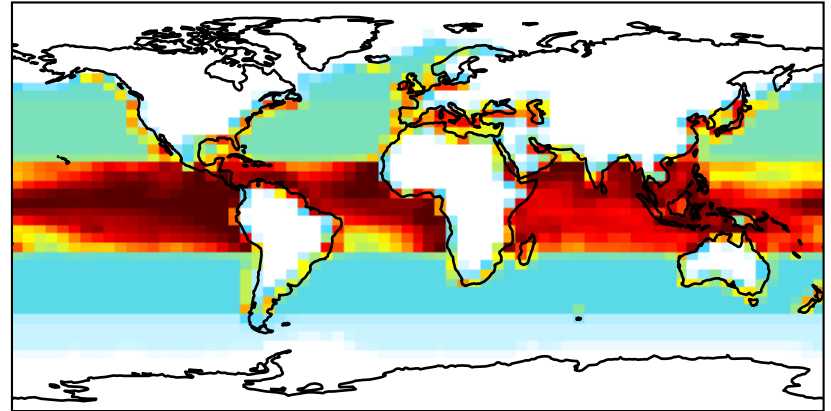


EmisCH2ICl_Ocean (Apr2019)

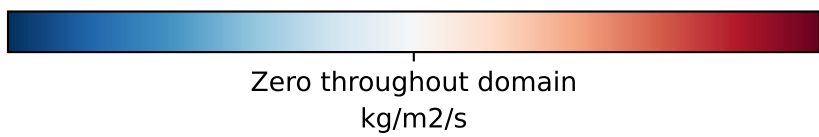
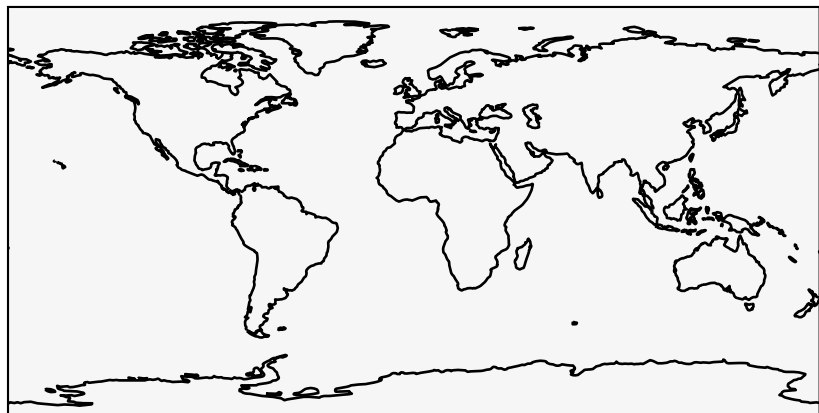
GCC_14.0.0 (Ref)
4.0x5.0



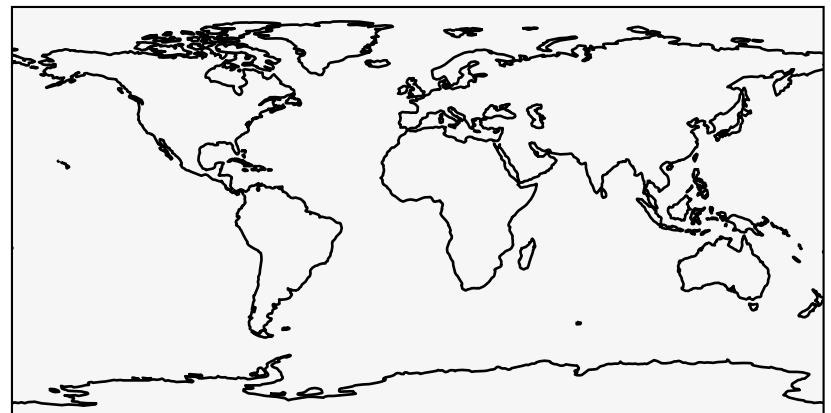
GCC_14.2.0 (Dev)
4.0x5.0



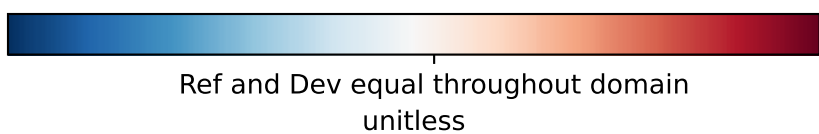
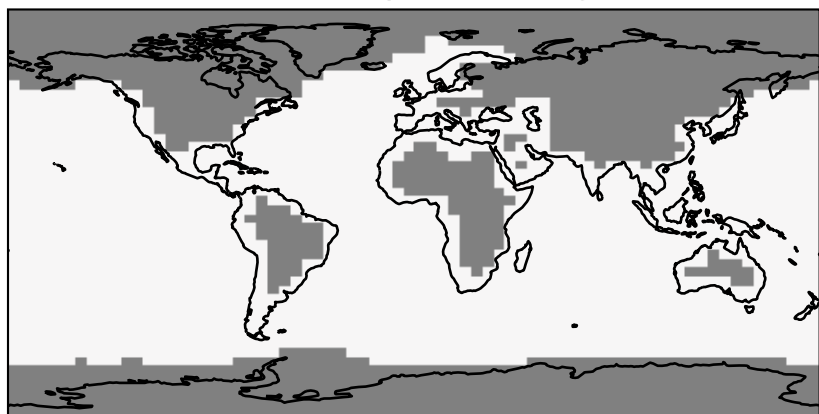
Difference
Dev - Ref, Dynamic Range



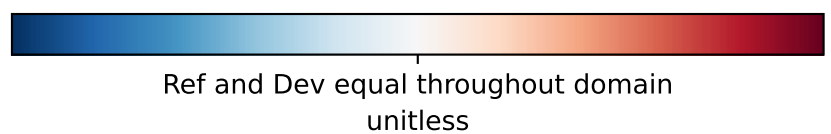
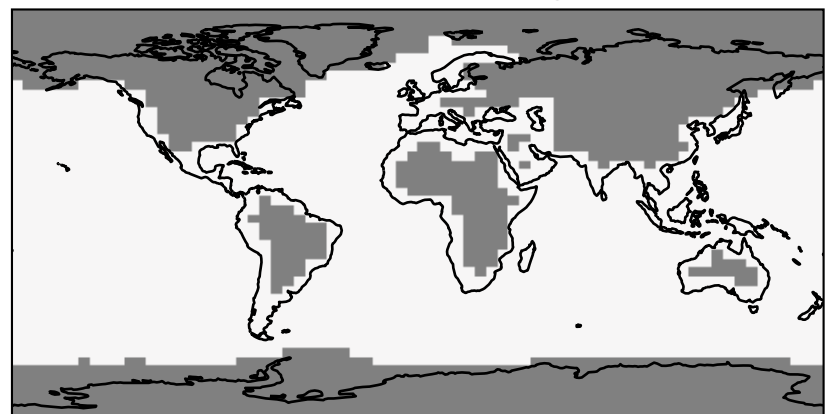
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

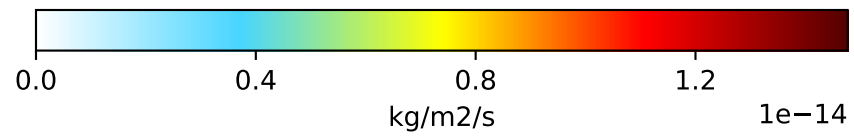
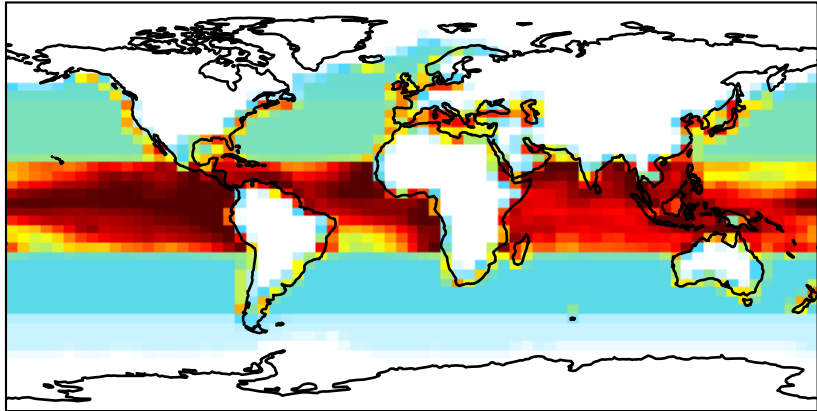


Ratio
Dev/Ref, Fixed Range

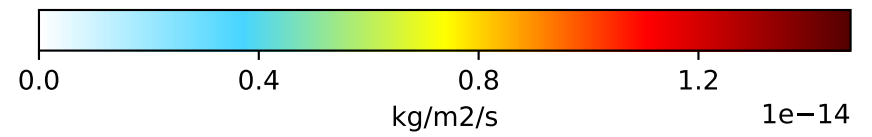
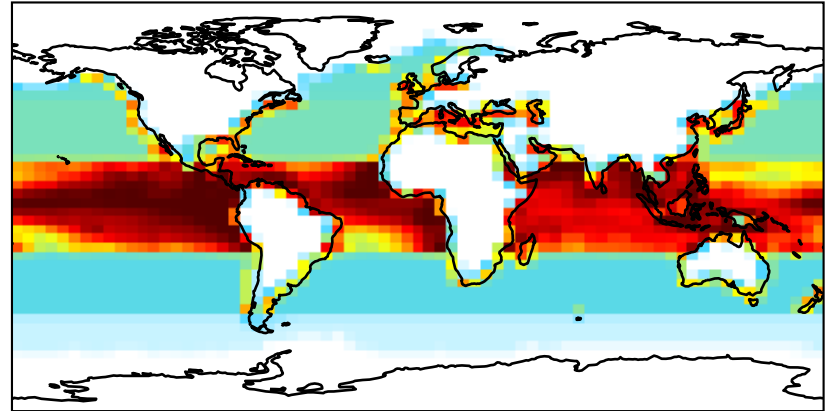


EmisCH2IBr_Ocean (Apr2019)

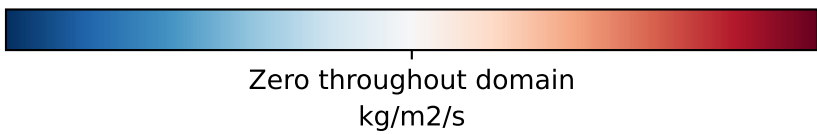
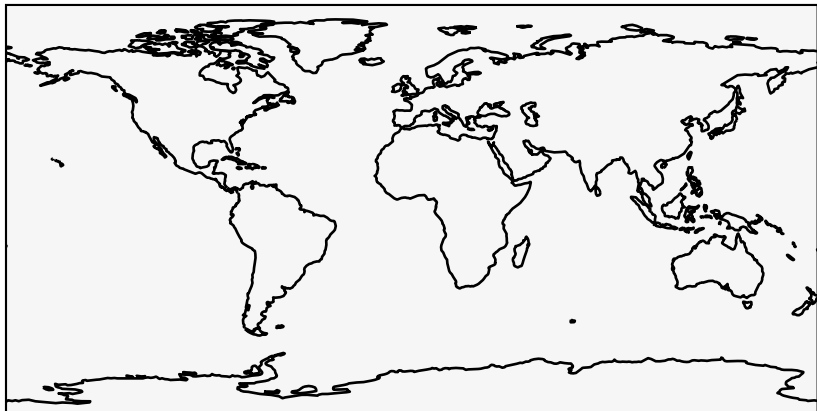
GCC_14.0.0 (Ref)
4.0x5.0



GCC_14.2.0 (Dev)
4.0x5.0



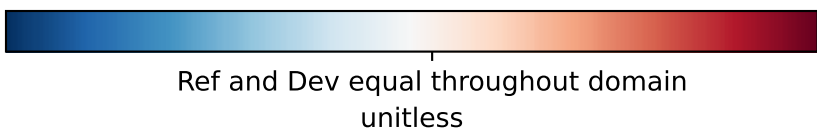
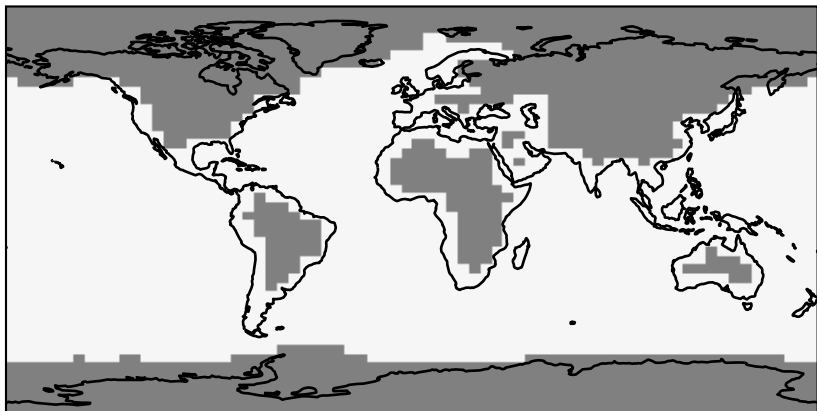
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

