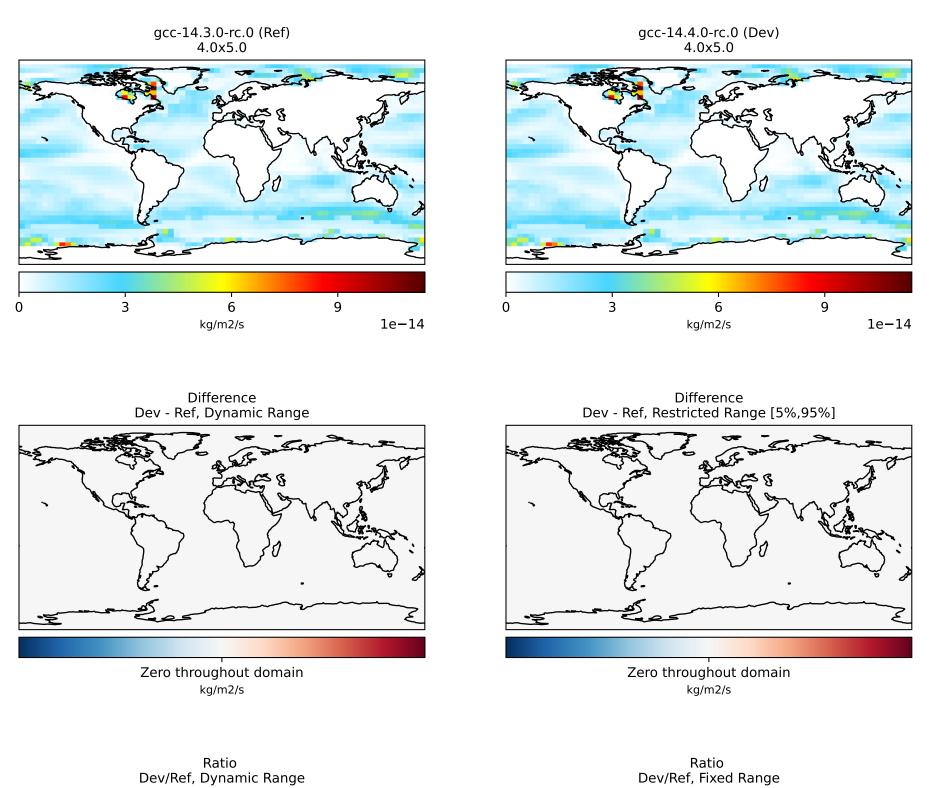
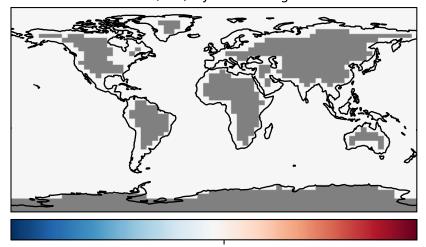
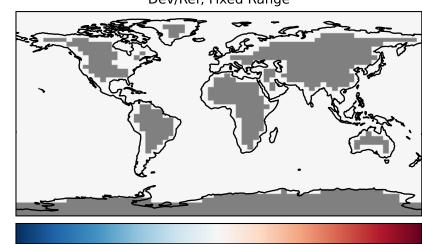
EmisBrSALA_Natural (Apr2019)



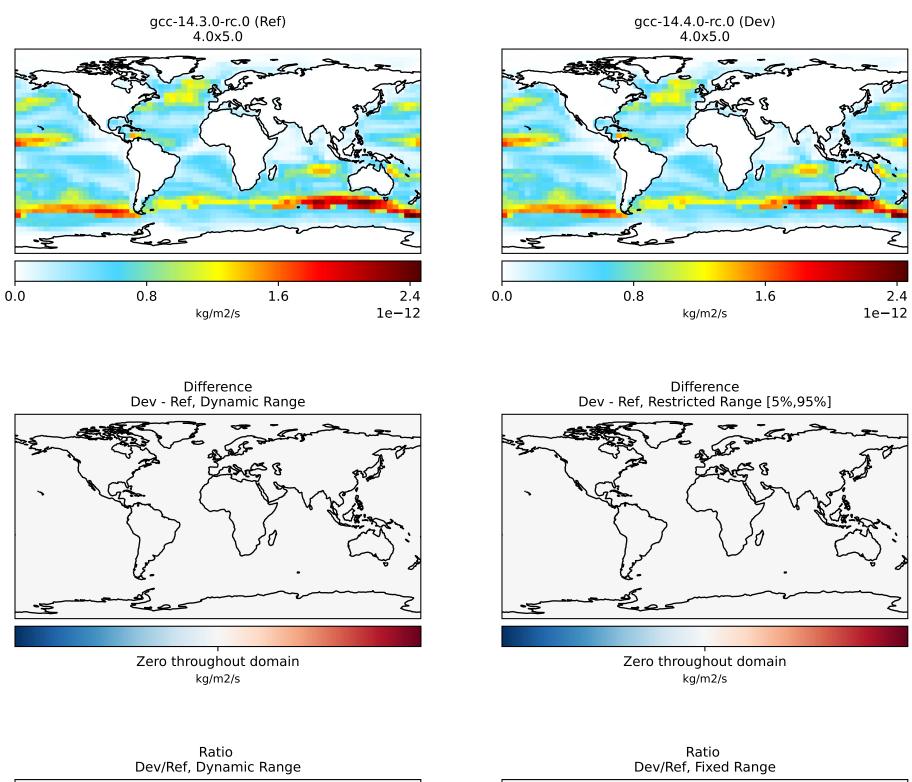


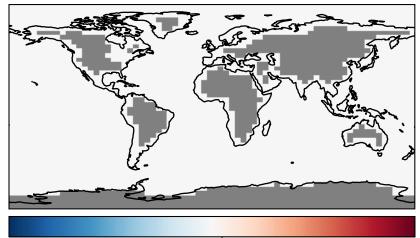
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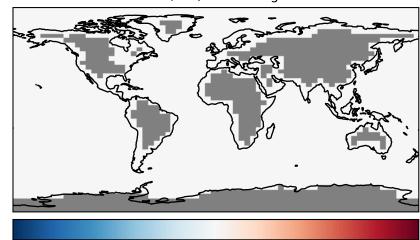
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EmisBrSALC_Natural (Apr2019)



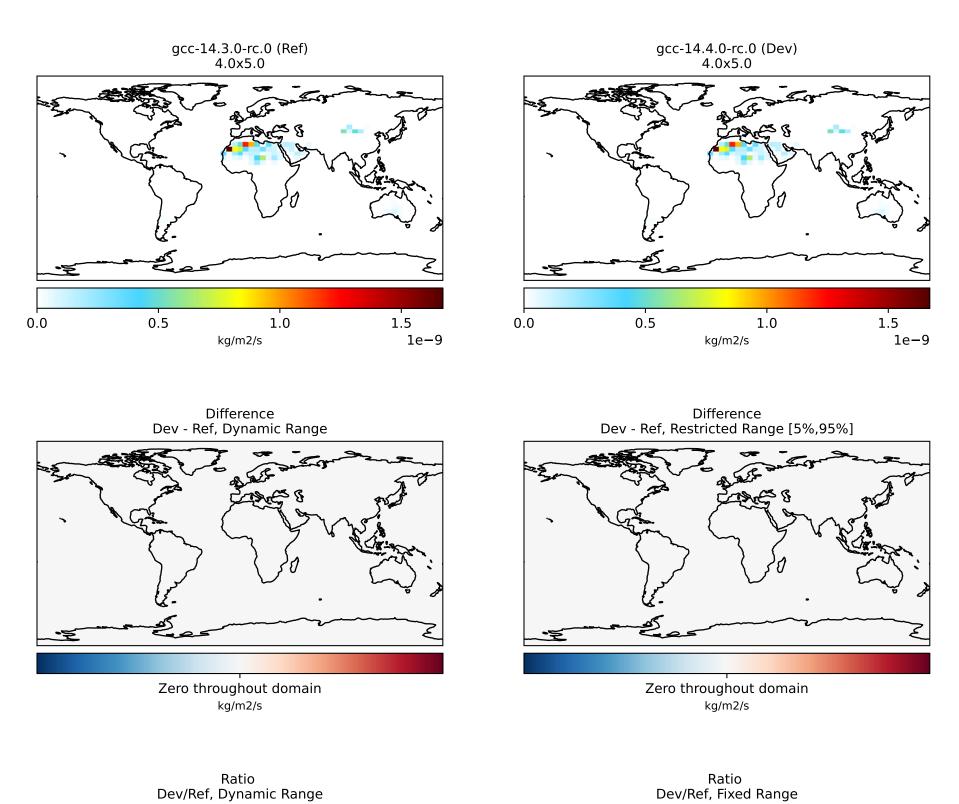


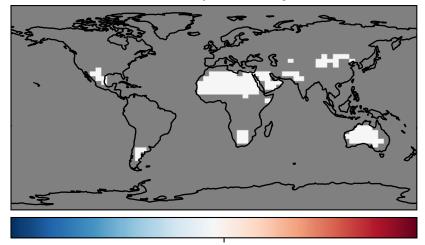
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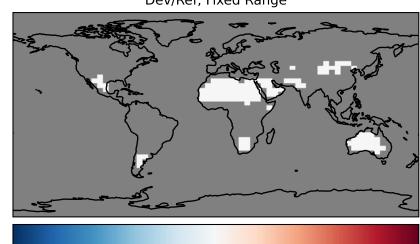
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EmisDST1_Natural (Apr2019)



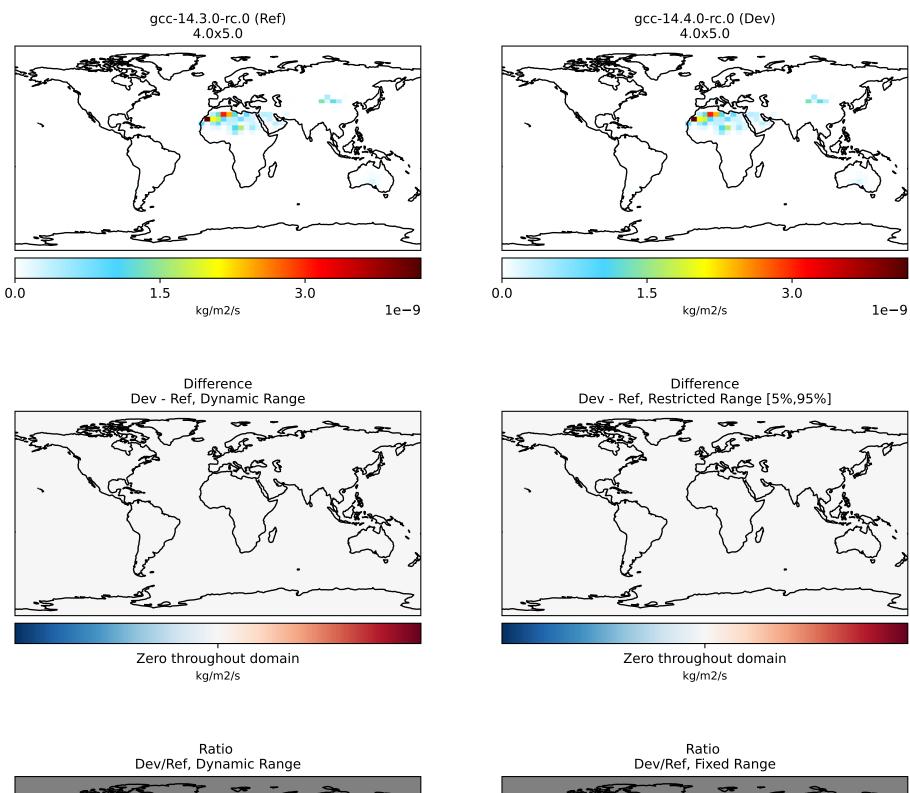


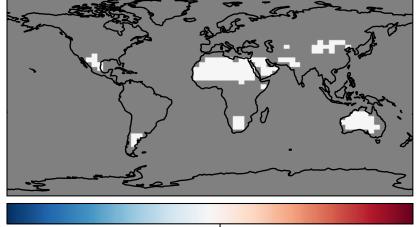
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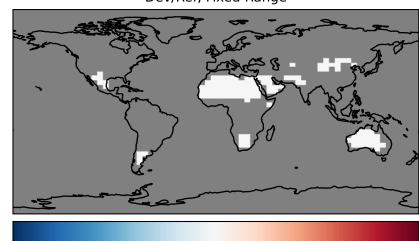
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EmisDST2_Natural (Apr2019)



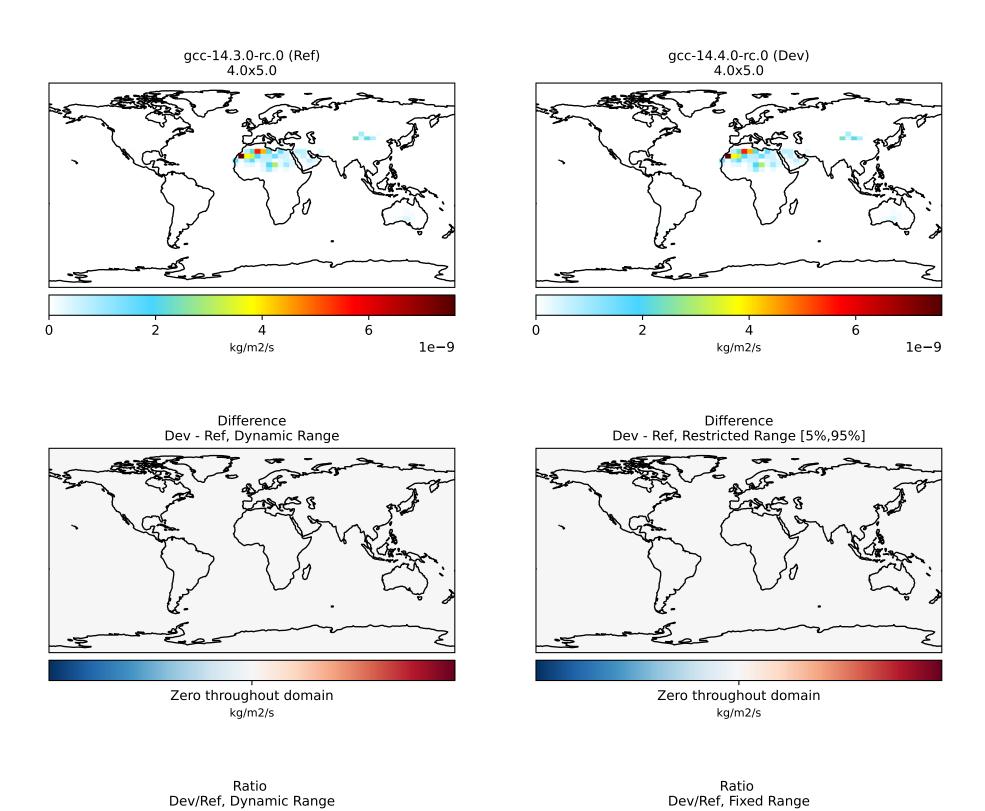


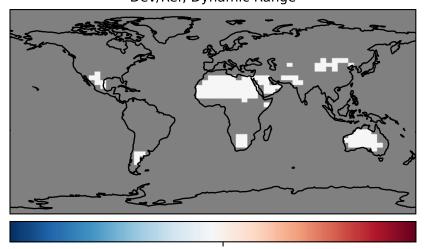
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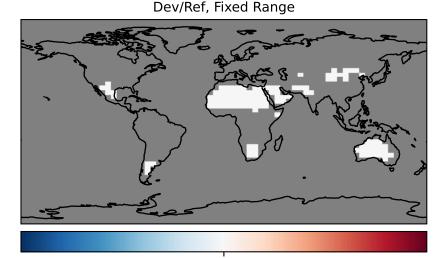
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EmisDST3_Natural (Apr2019)



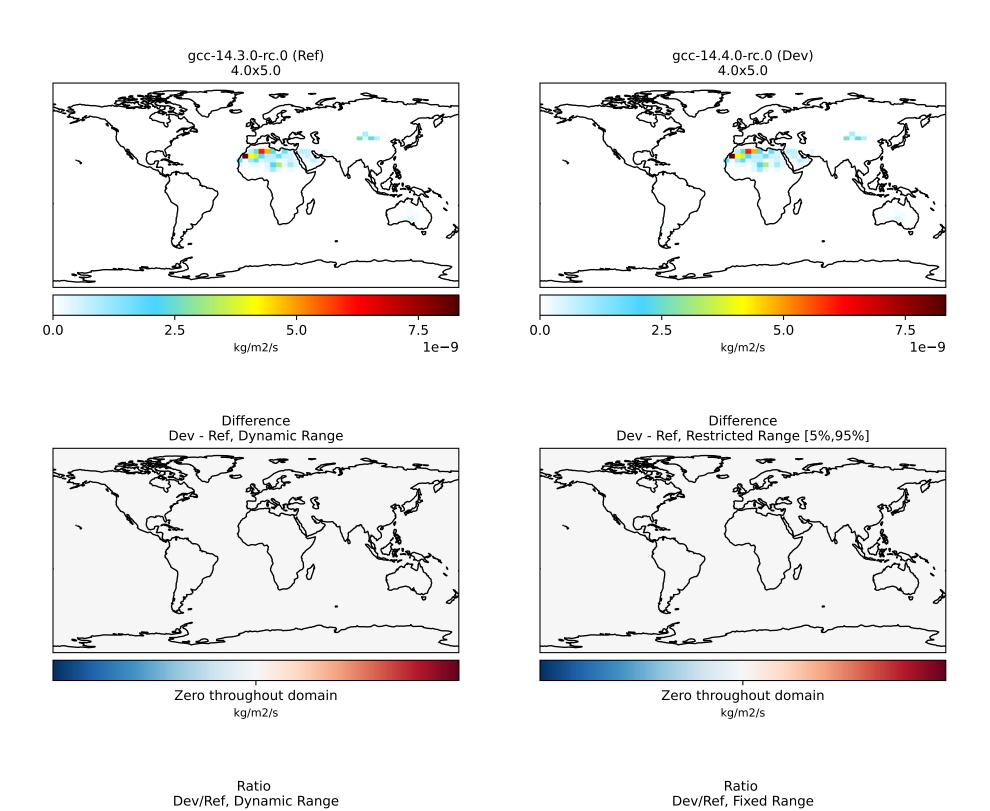


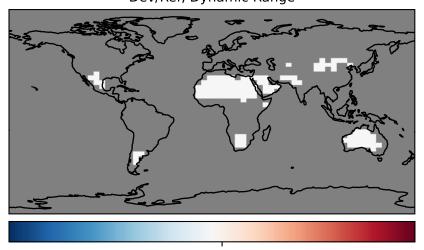
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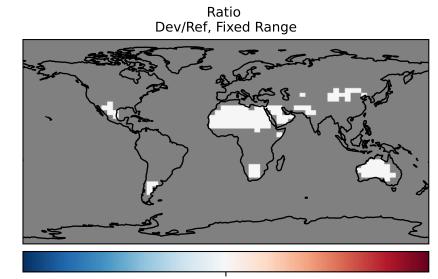
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EmisDST4_Natural (Apr2019)



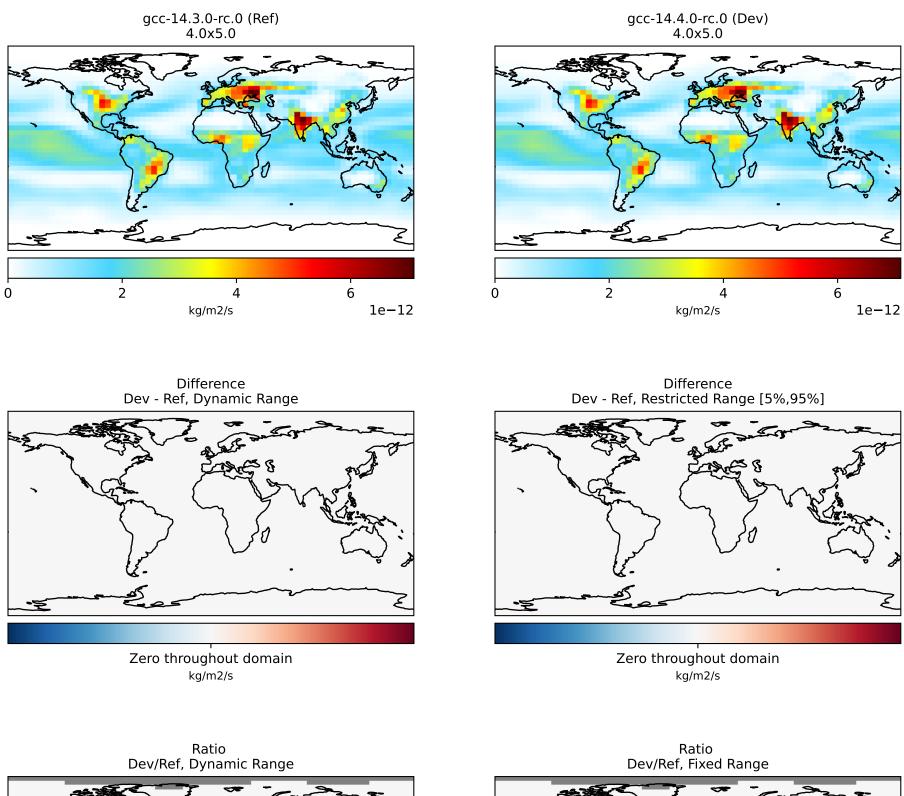


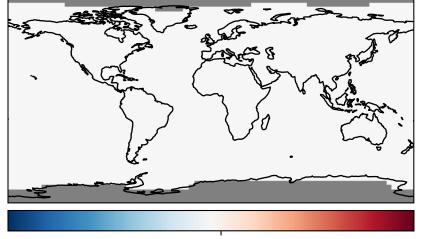
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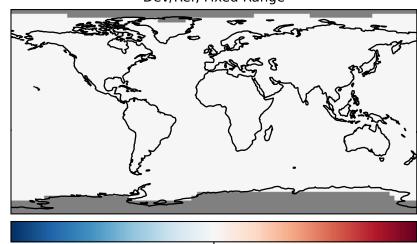
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EmisNH3_Natural (Apr2019)



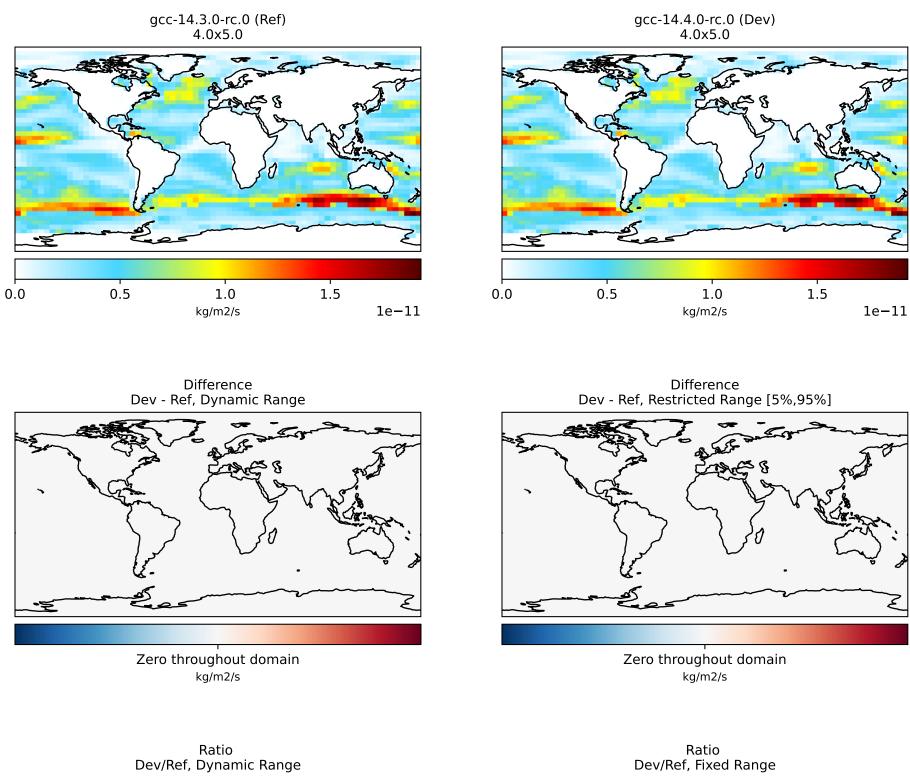


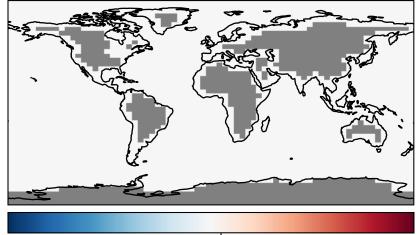
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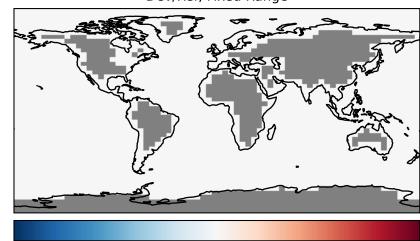
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EmisSALA_Natural (Apr2019)



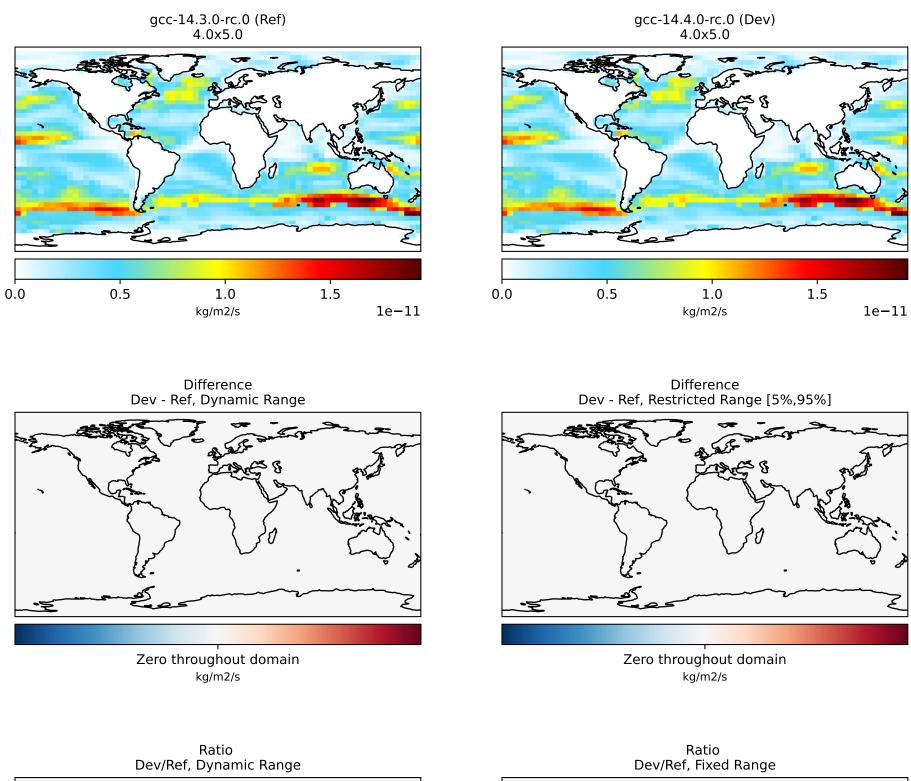


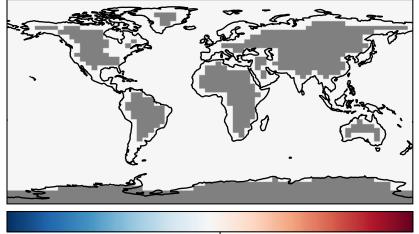
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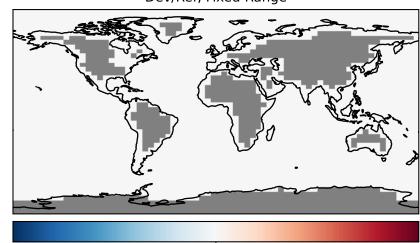
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EmisSALAAL_Natural (Apr2019)



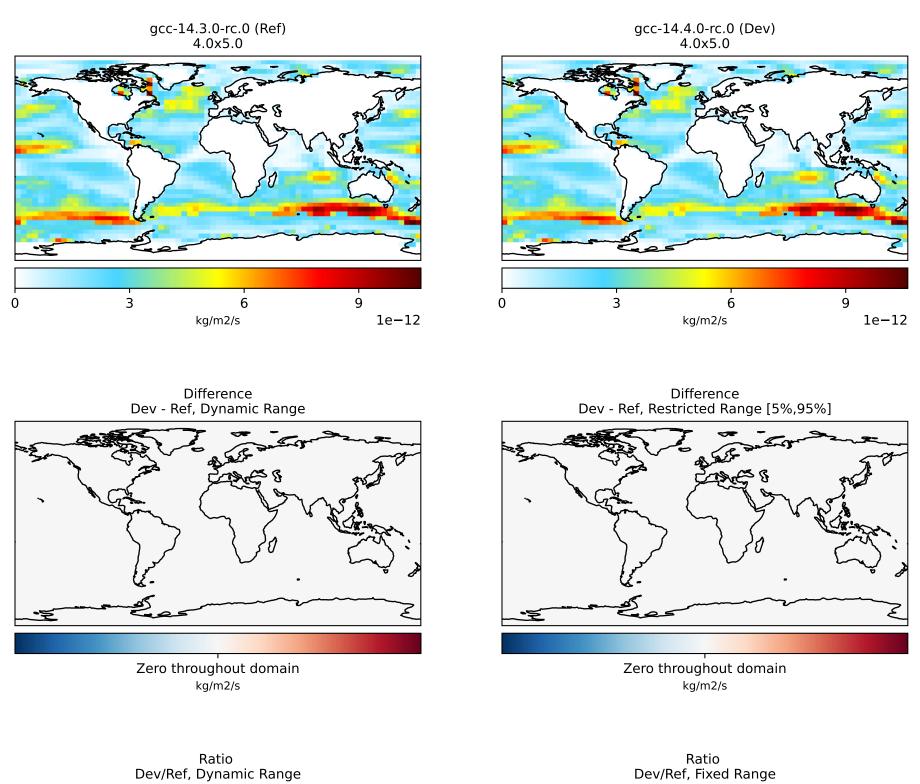


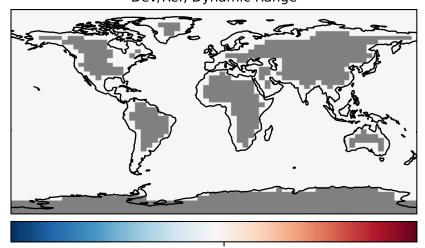
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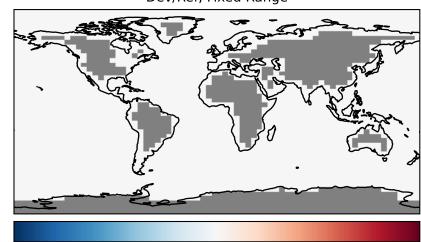
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EmisSALACL_Natural (Apr2019)



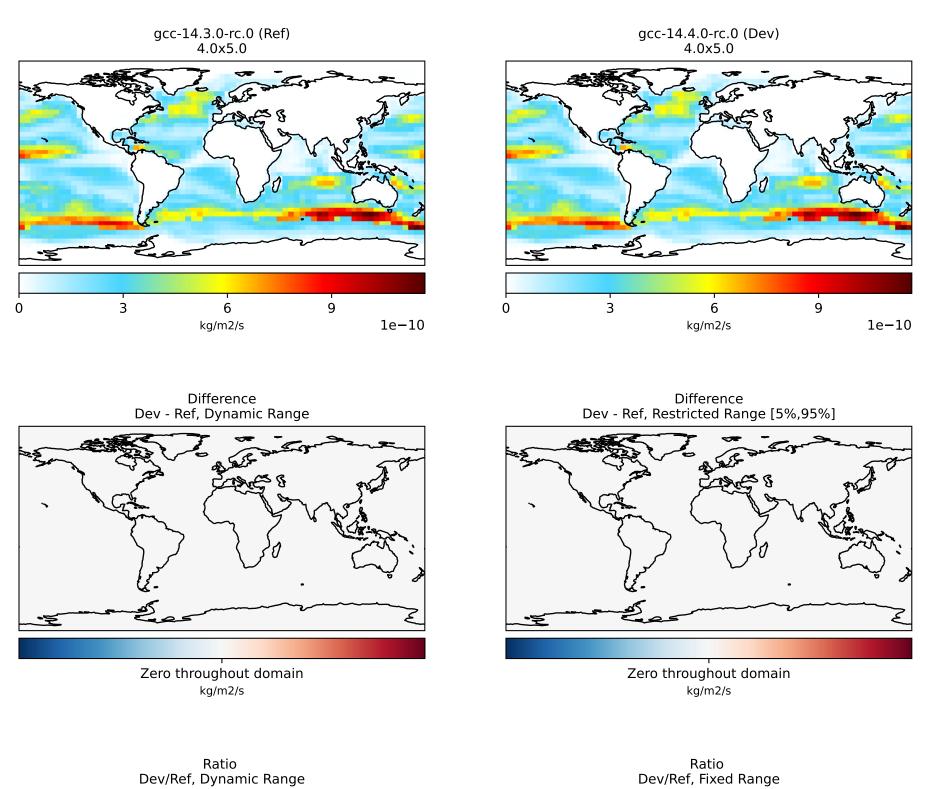


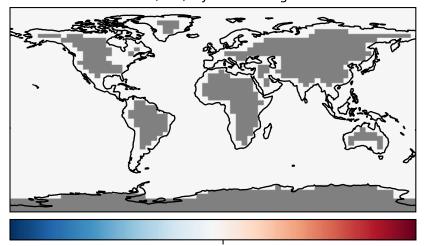
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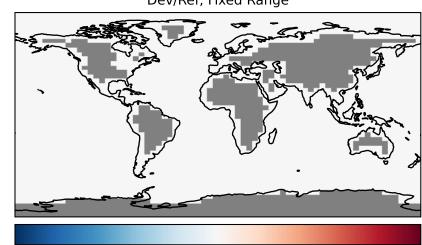
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EmisSALC_Natural (Apr2019)



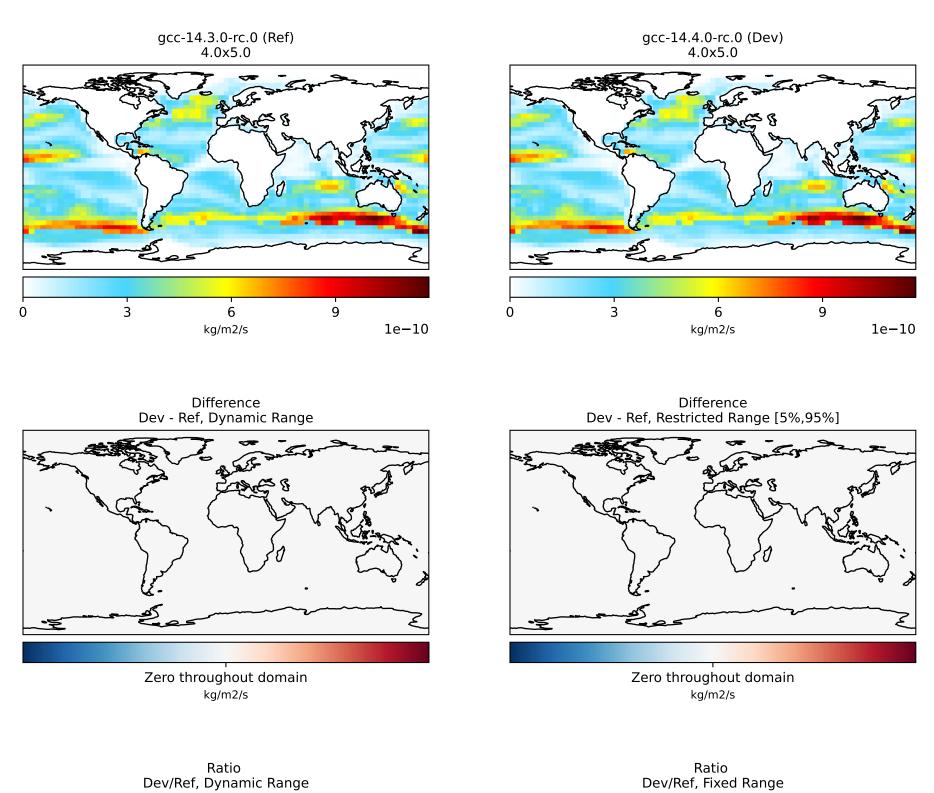


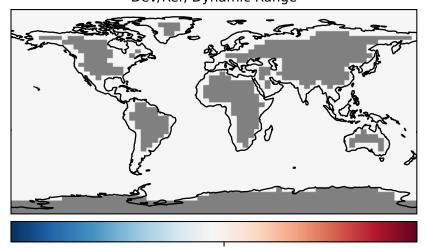
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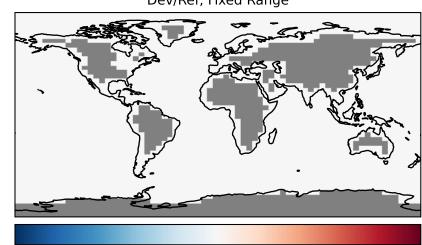
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EmisSALCAL_Natural (Apr2019)



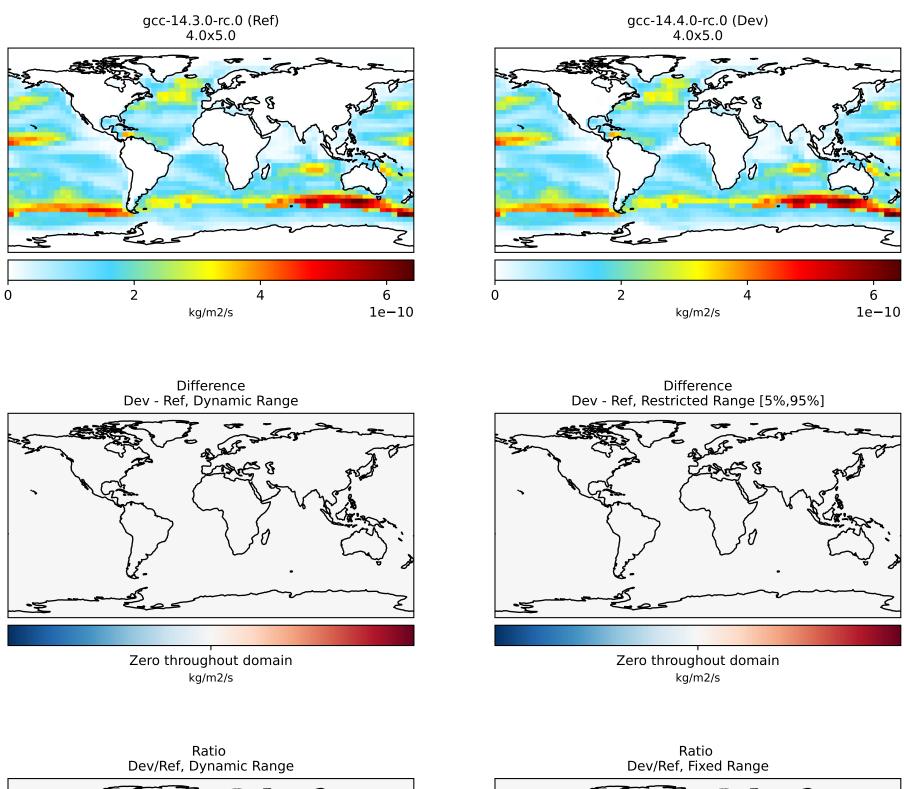


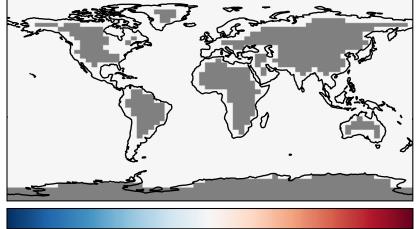
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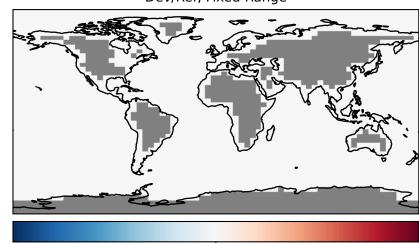
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EmisSALCCL_Natural (Apr2019)





Ref and Dev equal throughout domain unitless



Ref and Dev equal throughout domain unitless