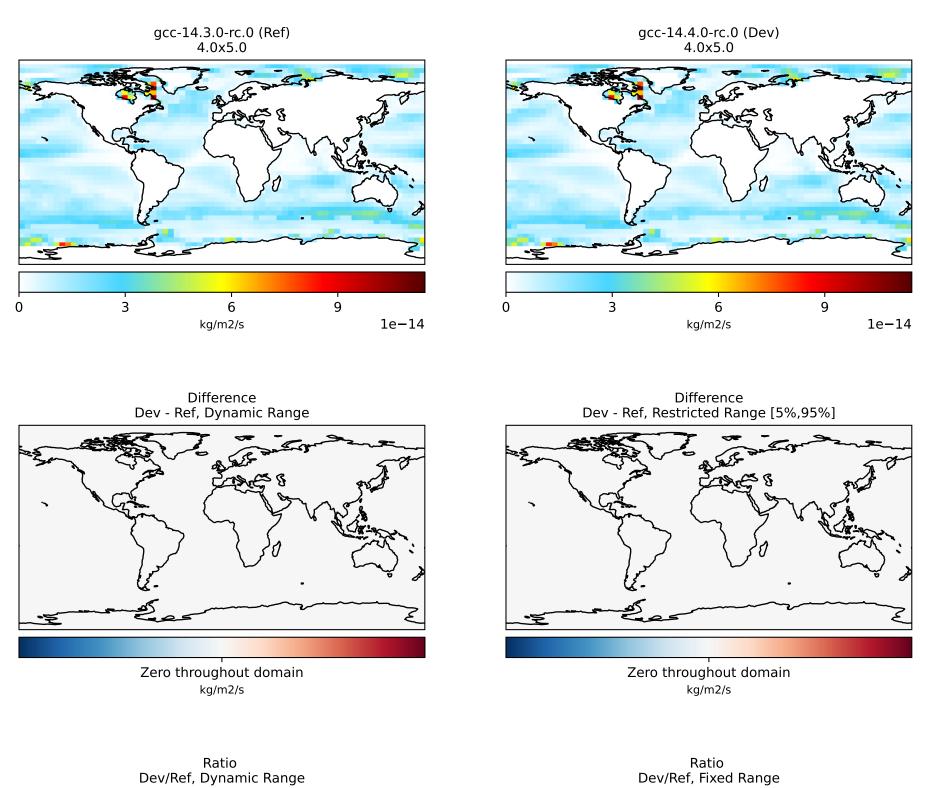
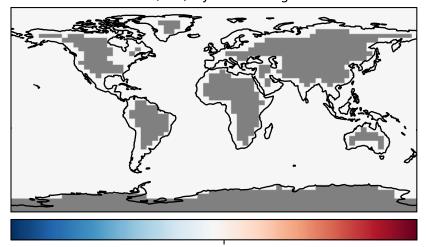
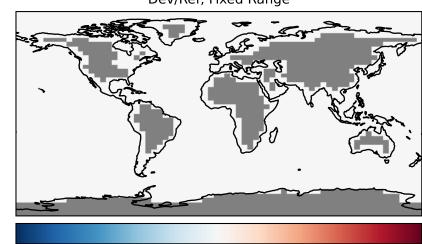
# EmisBrSALA\_Natural (Apr2019)



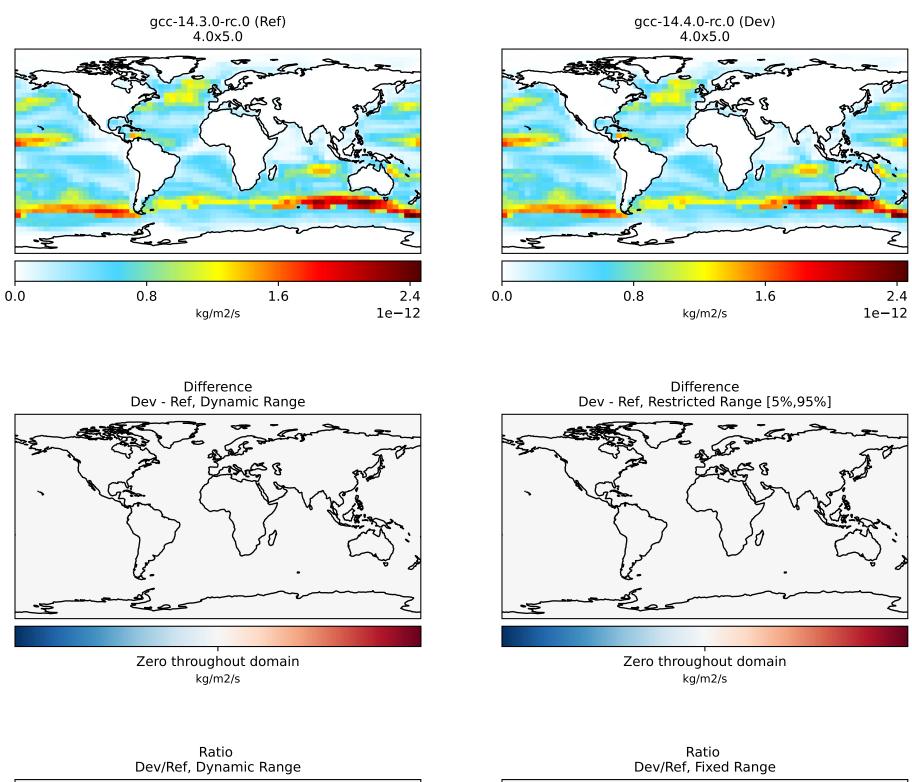


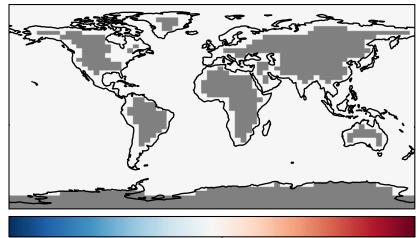
Ref and Dev equal throughout domain unitless



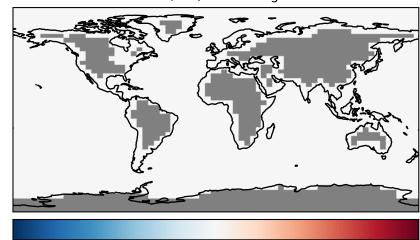
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## EmisBrSALC\_Natural (Apr2019)



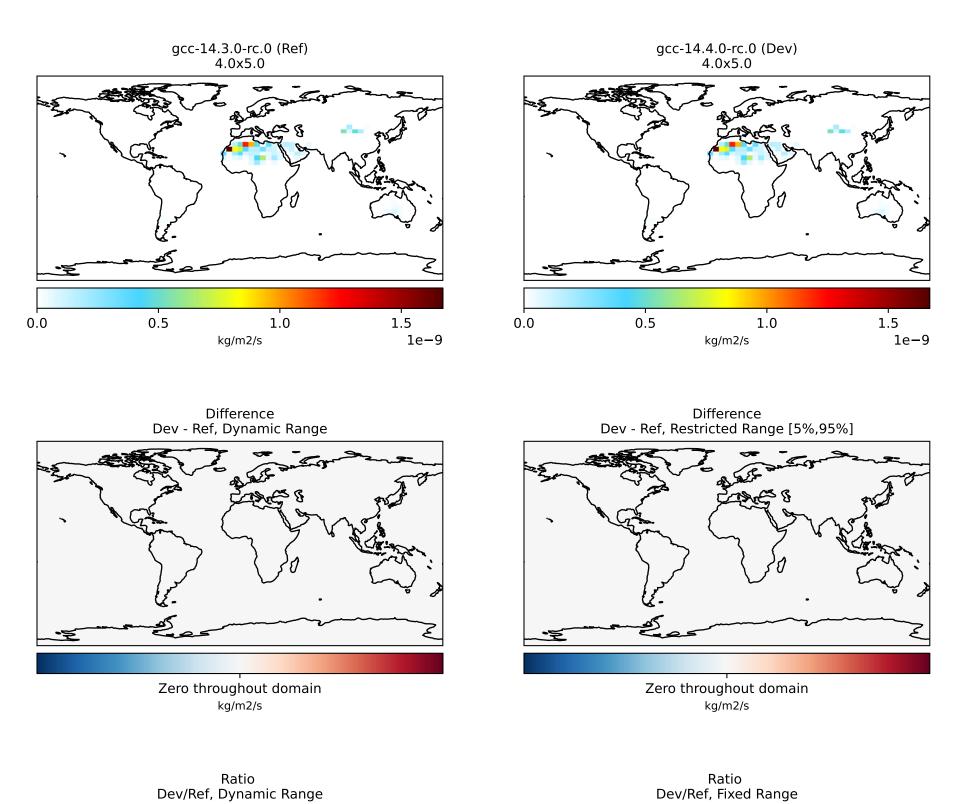


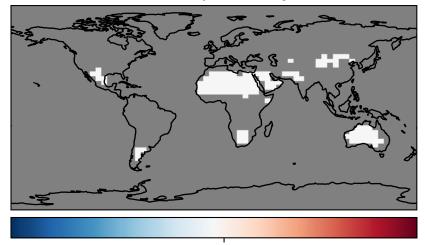
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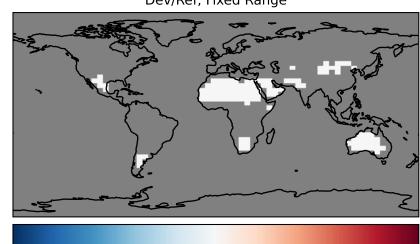
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## EmisDST1\_Natural (Apr2019)



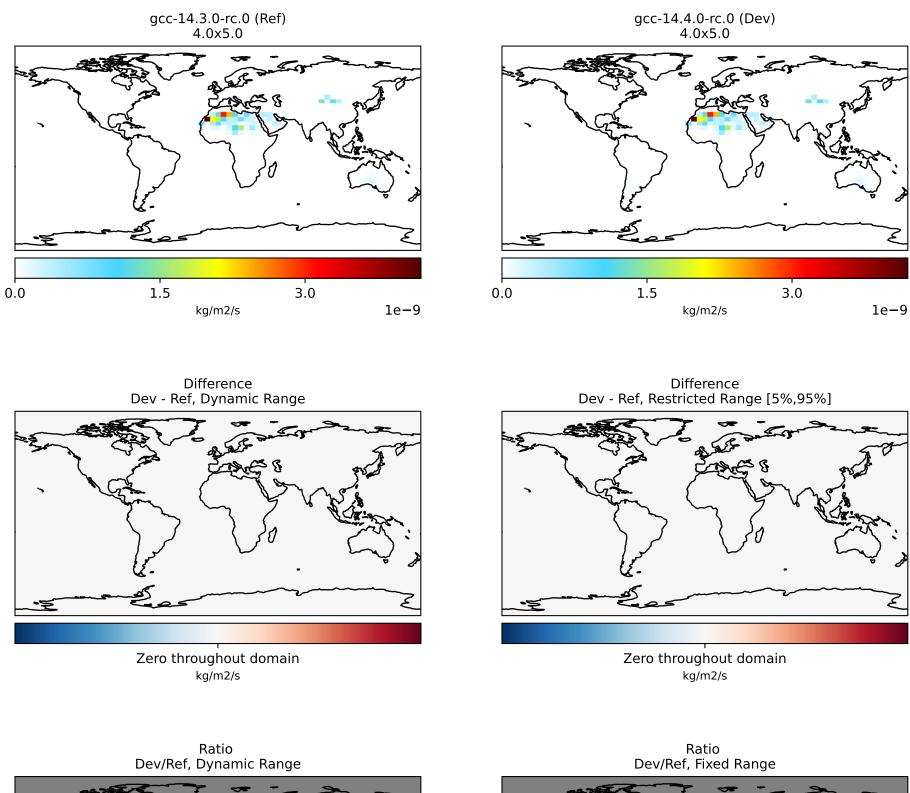


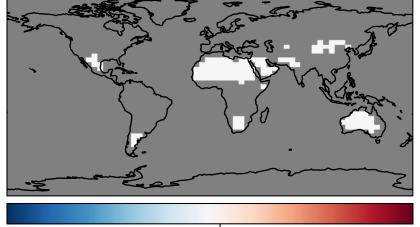
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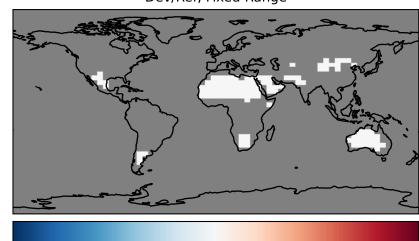
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## EmisDST2\_Natural (Apr2019)



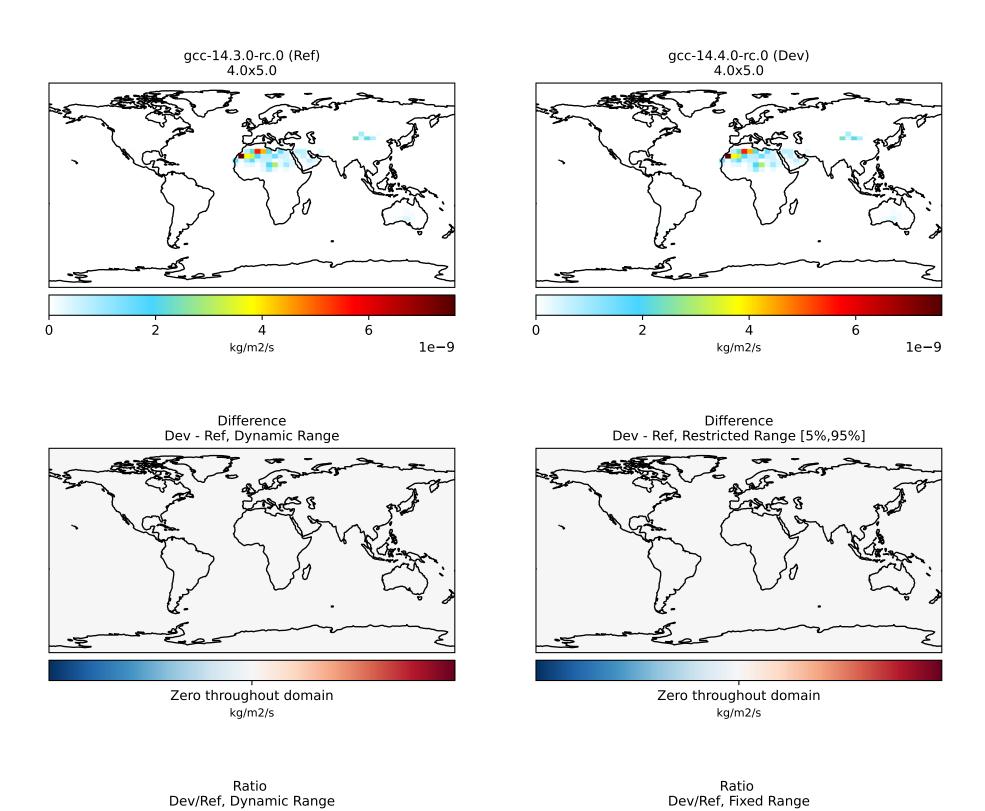


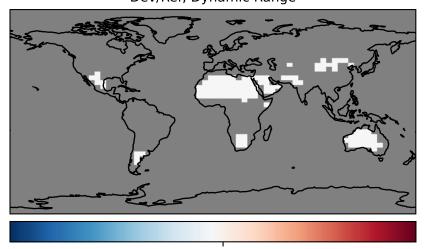
Ref and Dev equal throughout domain unitless



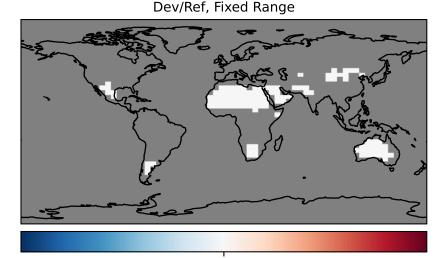
Ref and Dev equal throughout domain unitless

## EmisDST3\_Natural (Apr2019)



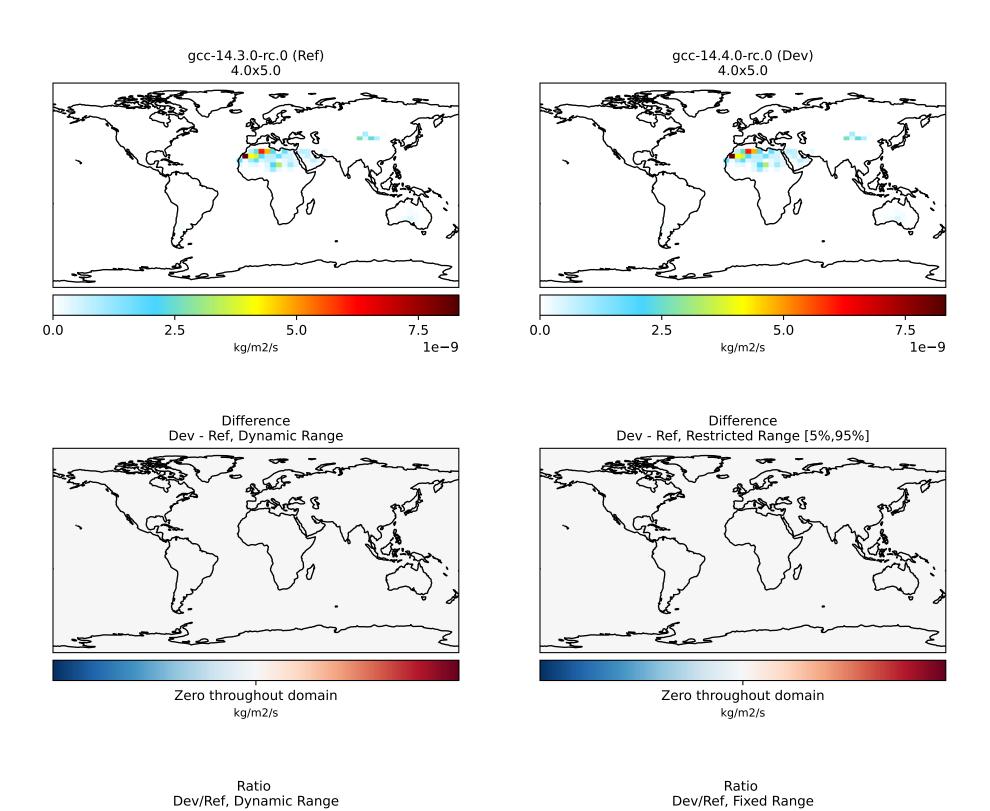


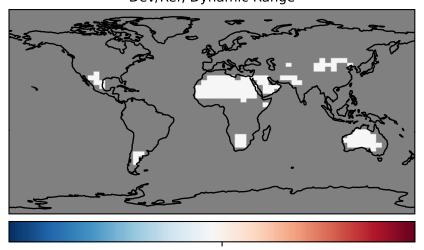
Ref and Dev equal throughout domain unitless



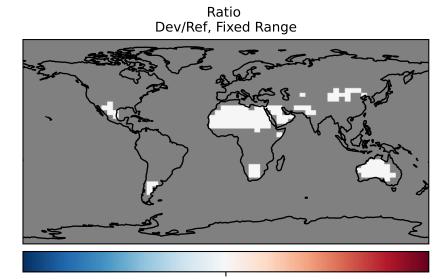
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## EmisDST4\_Natural (Apr2019)



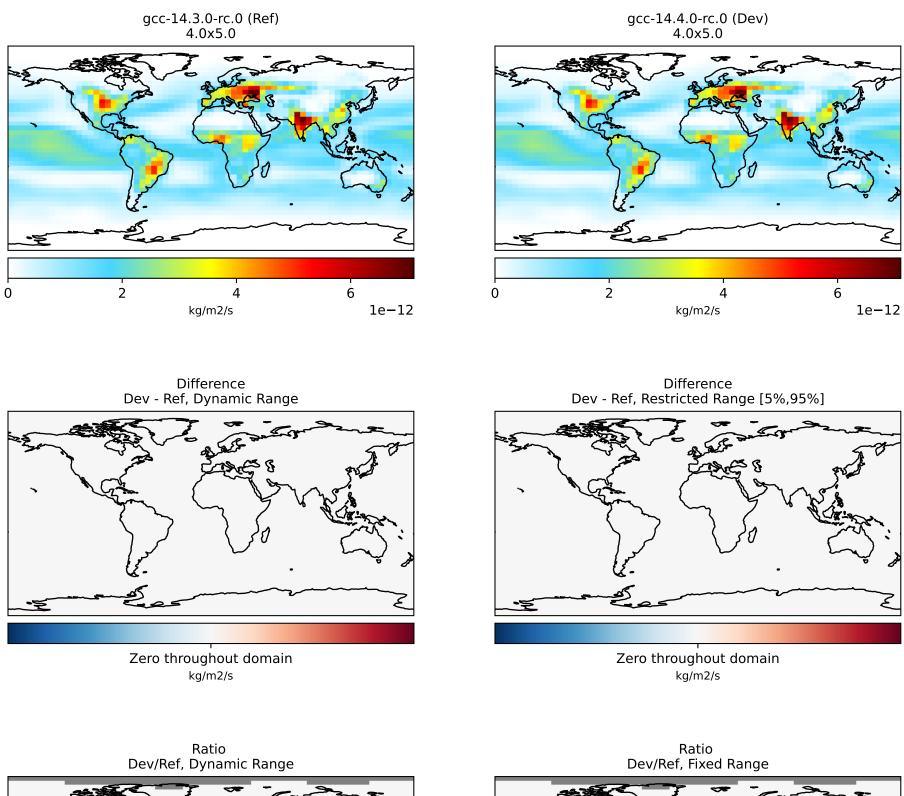


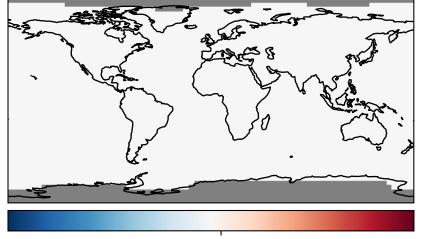
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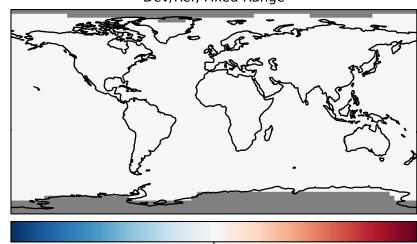
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## EmisNH3\_Natural (Apr2019)



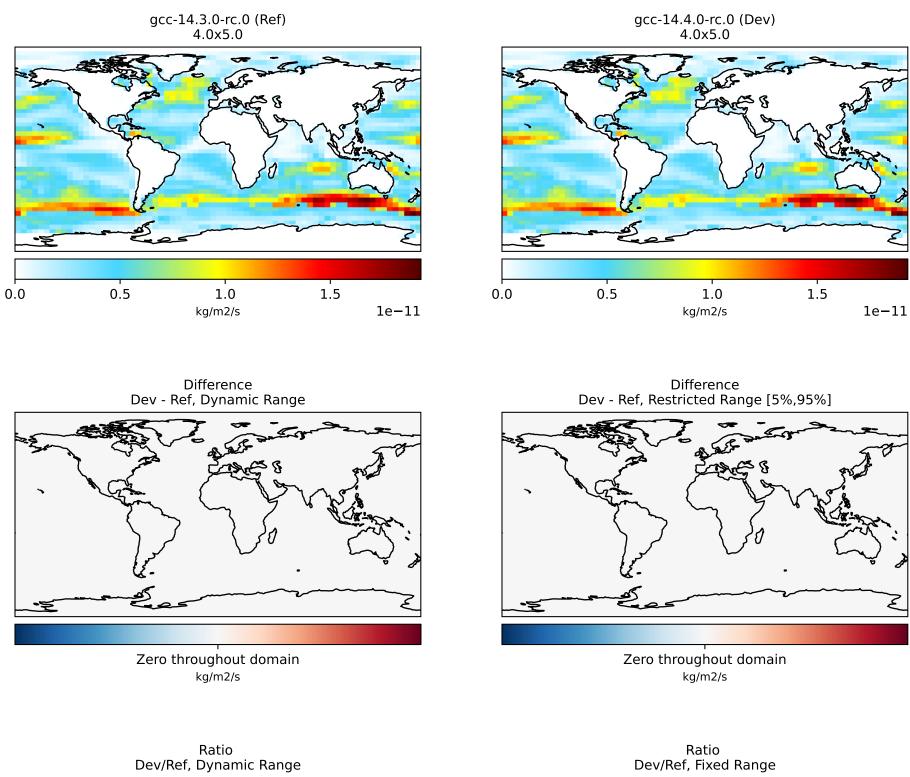


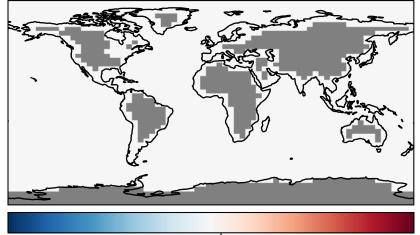
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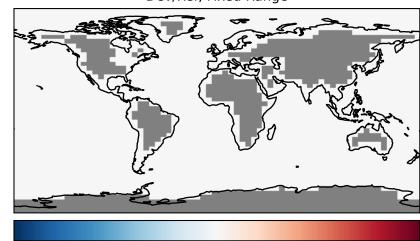
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#### EmisSALA\_Natural (Apr2019)



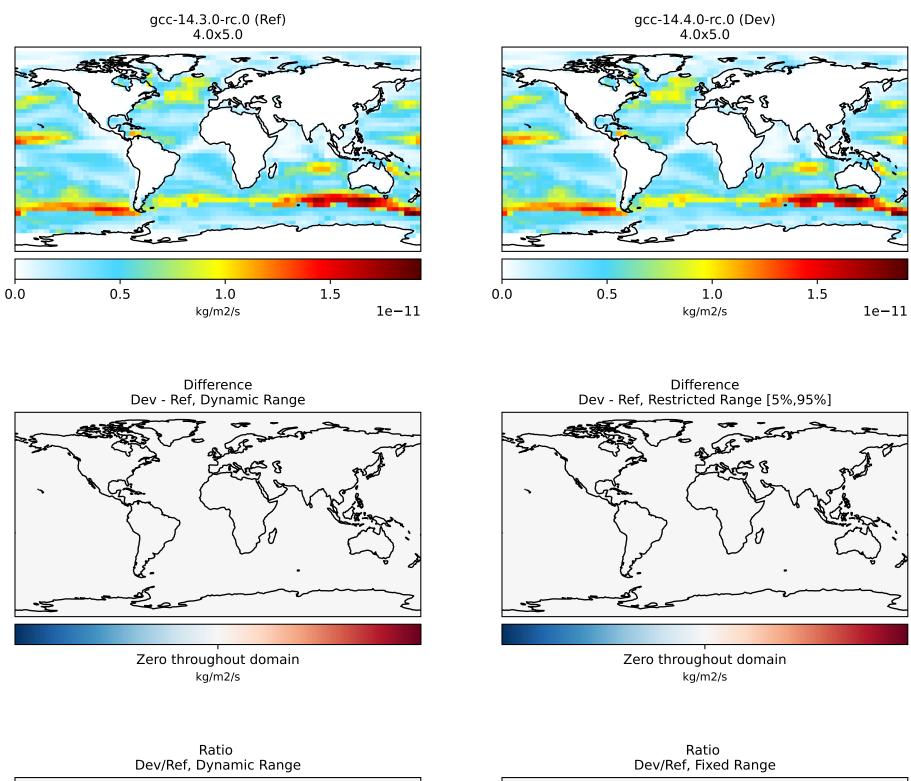


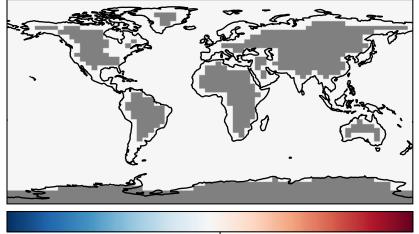
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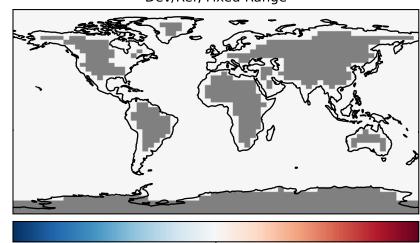
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#### EmisSALAAL\_Natural (Apr2019)



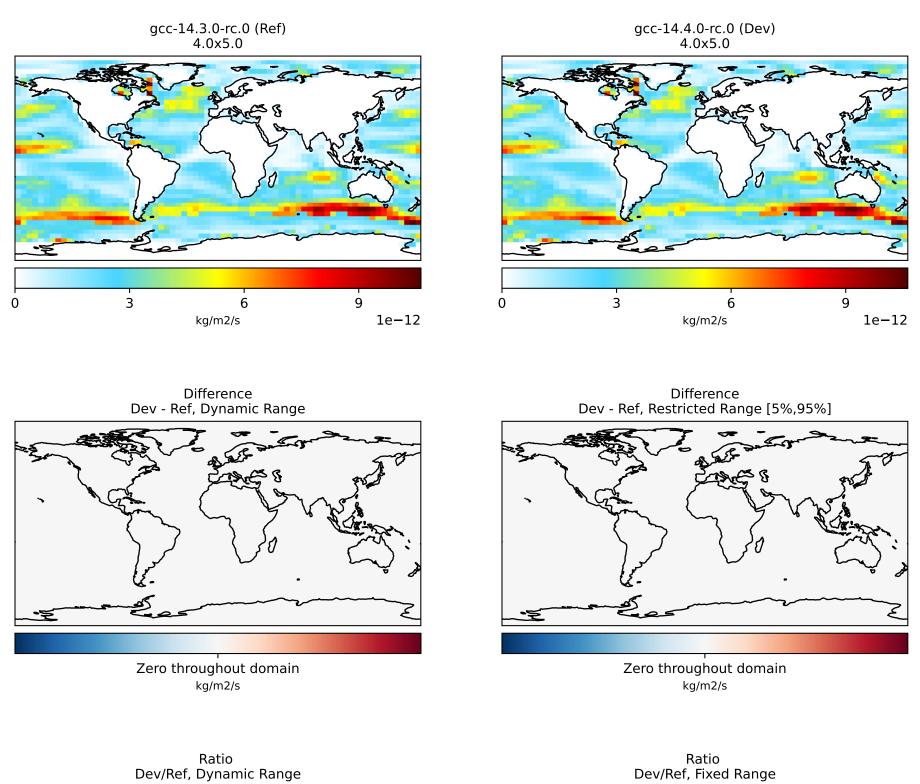


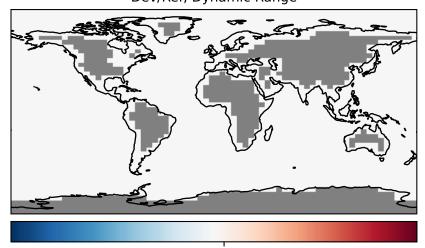
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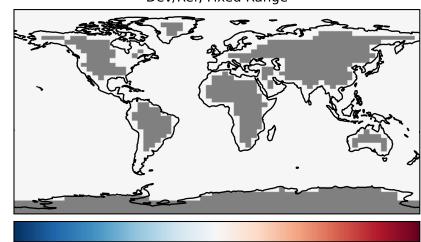
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## EmisSALACL\_Natural (Apr2019)



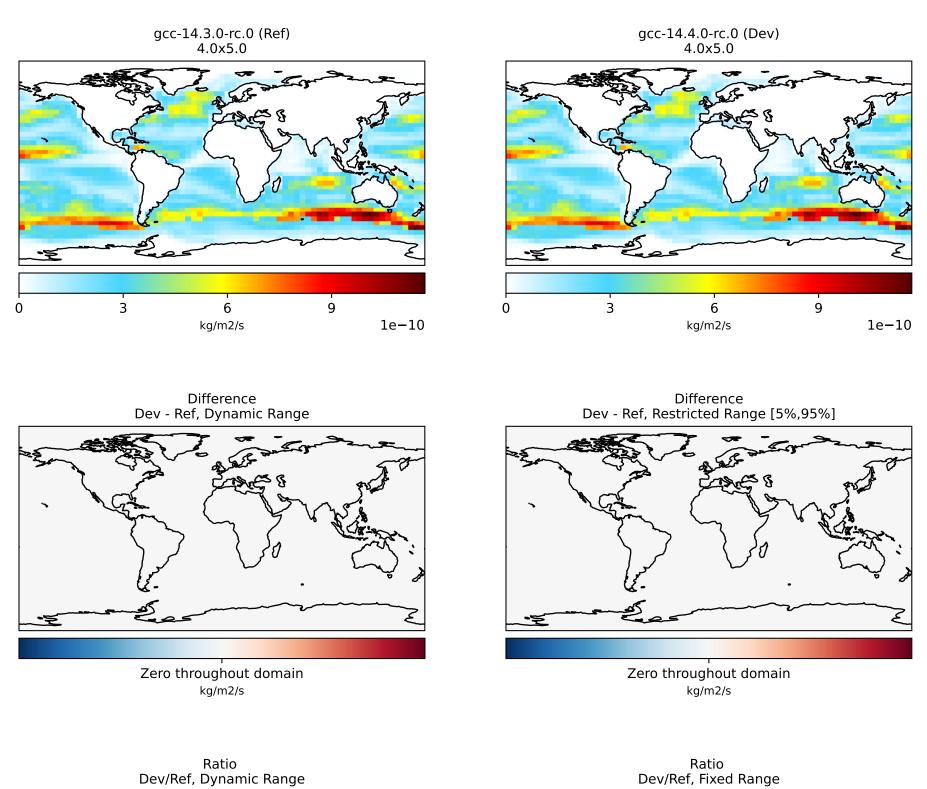


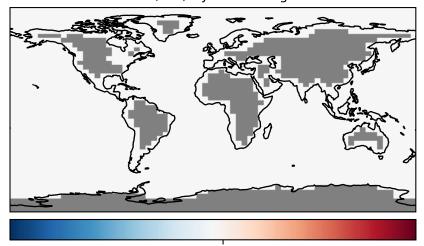
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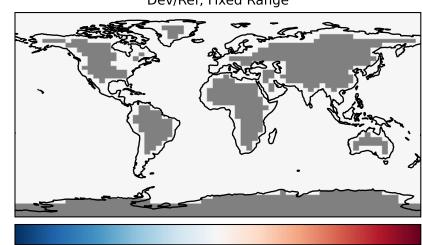
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## EmisSALC\_Natural (Apr2019)



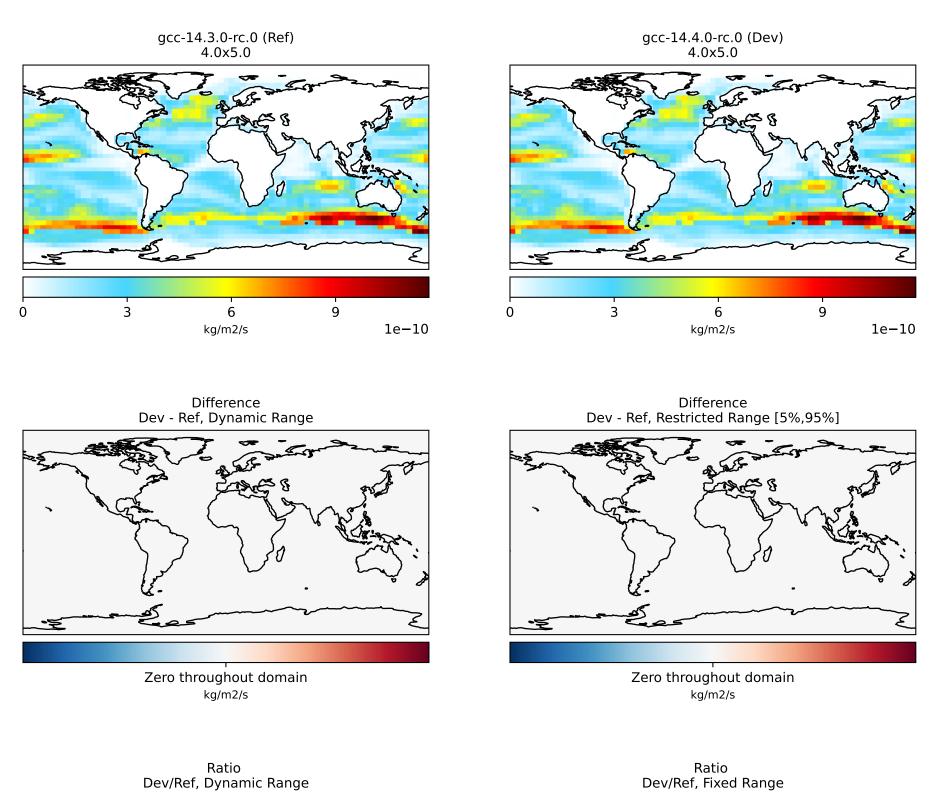


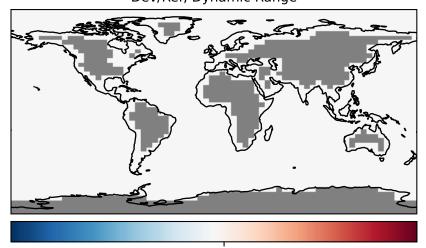
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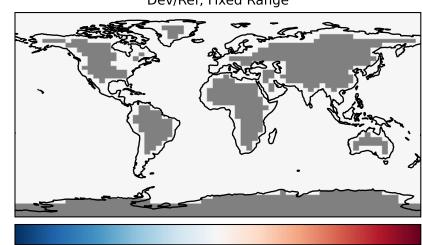
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## EmisSALCAL\_Natural (Apr2019)



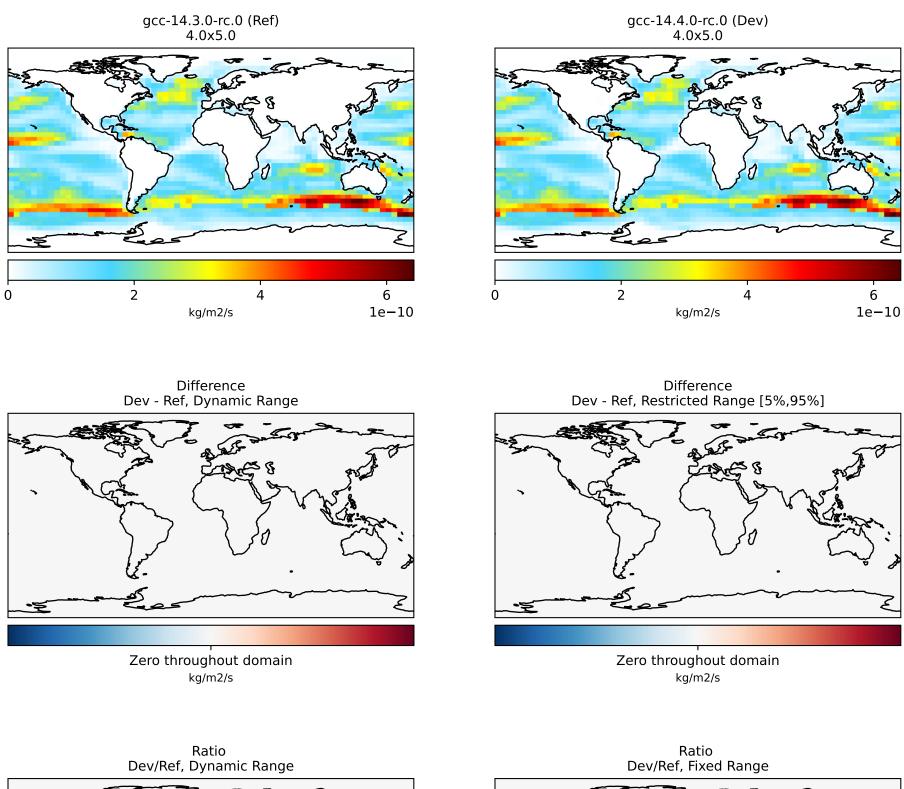


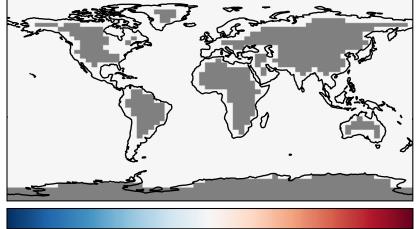
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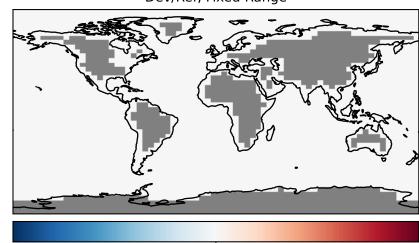
Ref and Dev equal throughout domain unitless

## EmisSALCCL\_Natural (Apr2019)





Ref and Dev equal throughout domain unitless



Ref and Dev equal throughout domain unitless