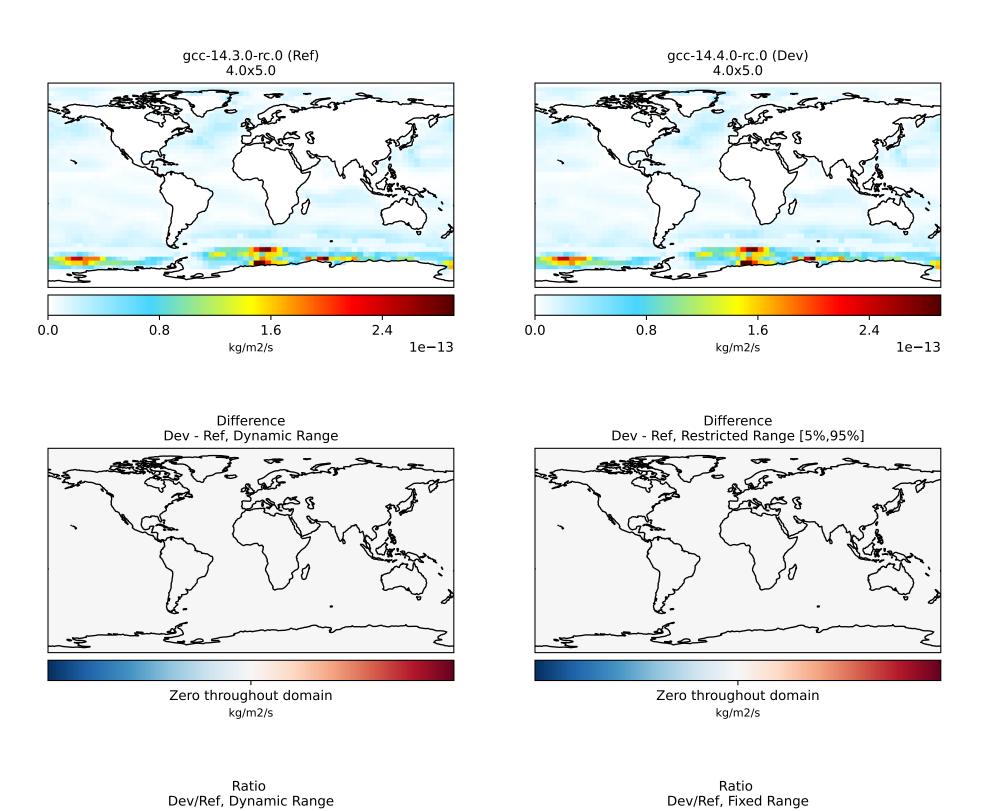
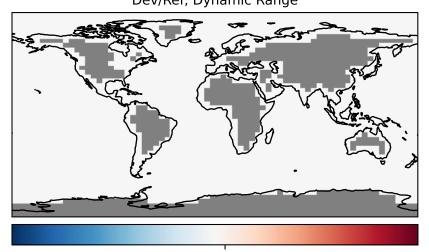
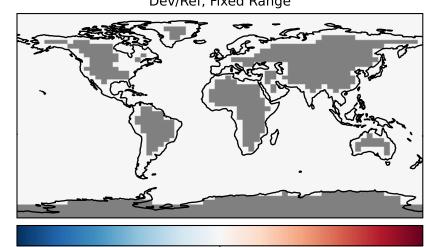
EmisBrSALA_Natural (Oct2019)



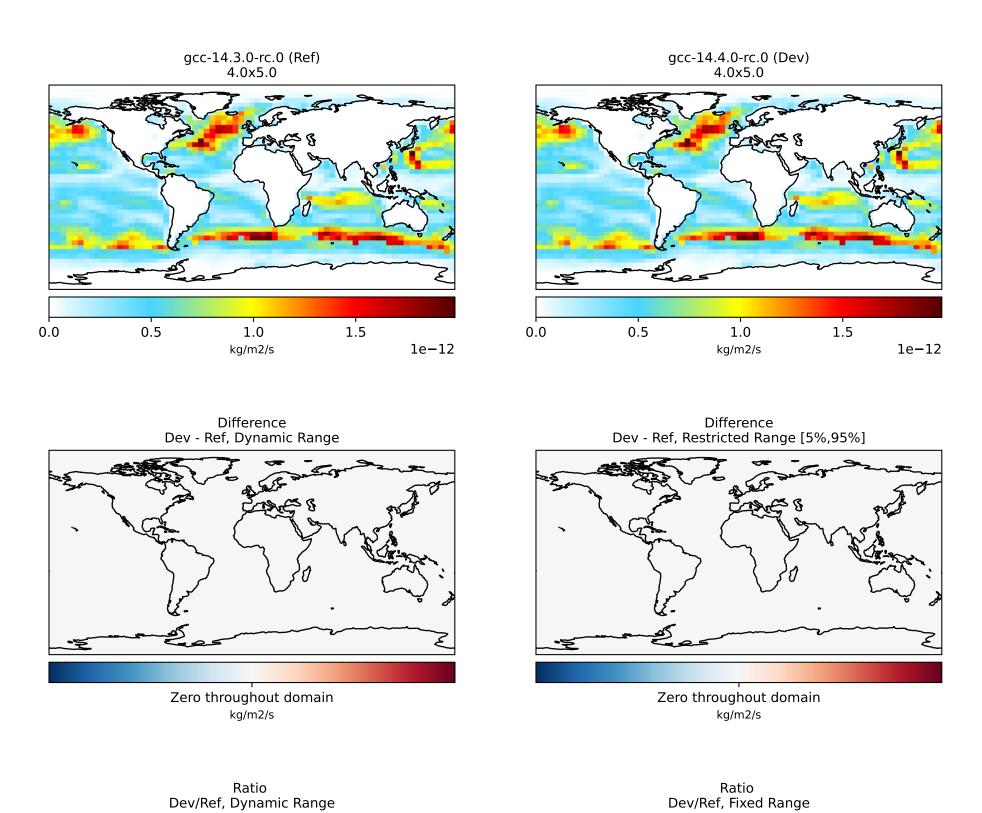


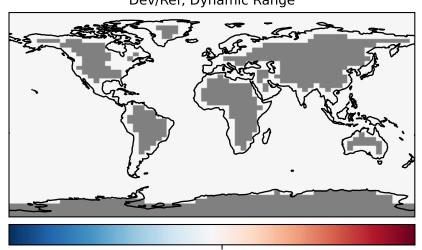
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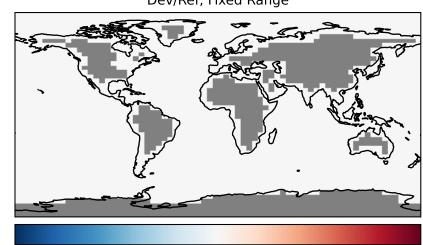
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EmisBrSALC_Natural (Oct2019)



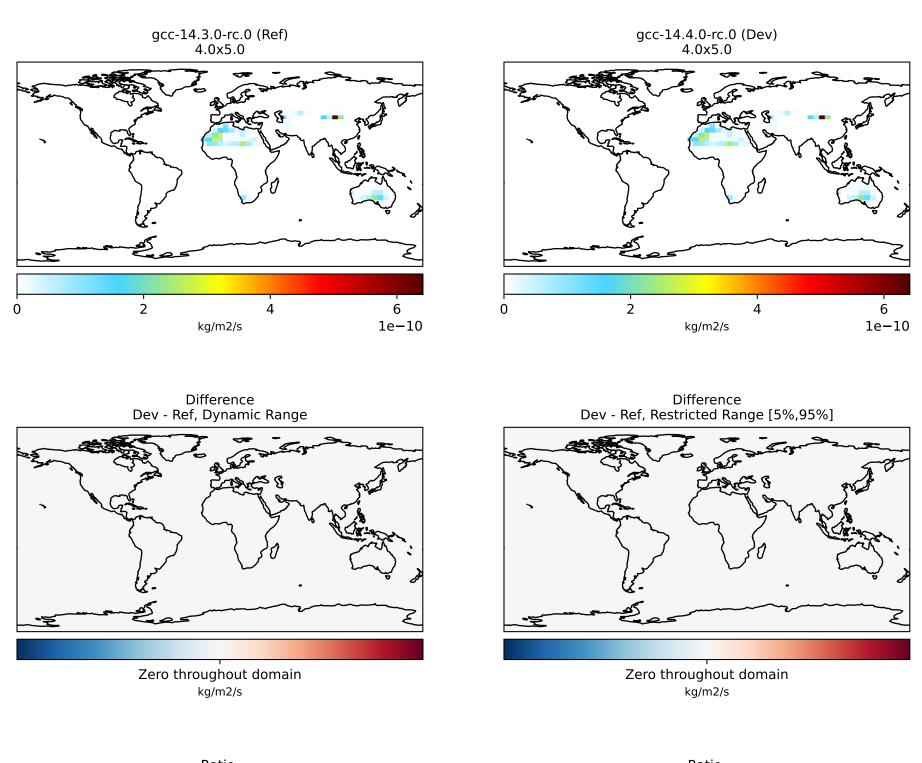


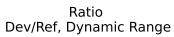
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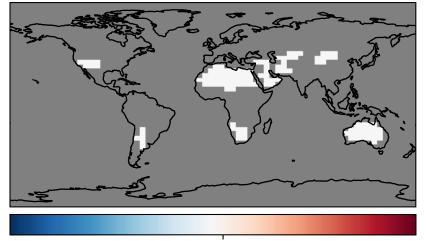


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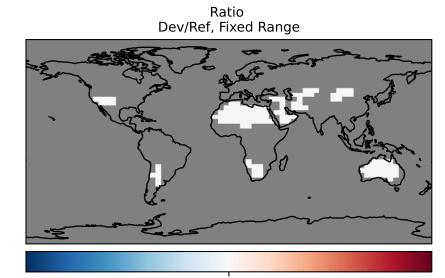
EmisDST1_Natural (Oct2019)





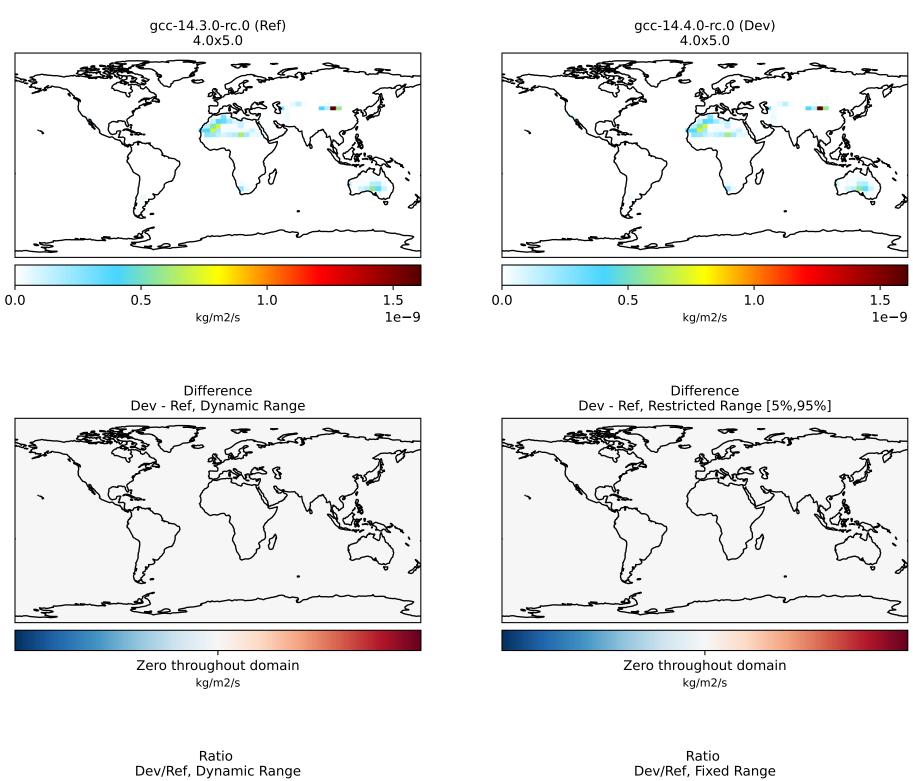


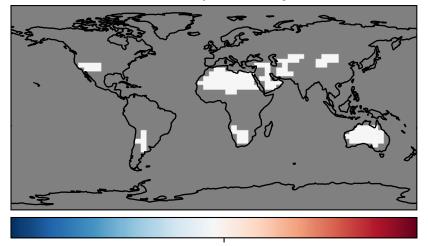
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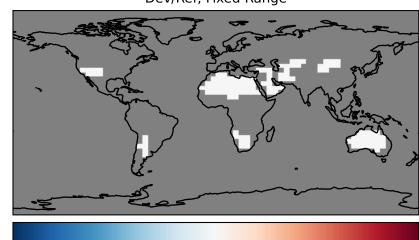
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EmisDST2_Natural (Oct2019)



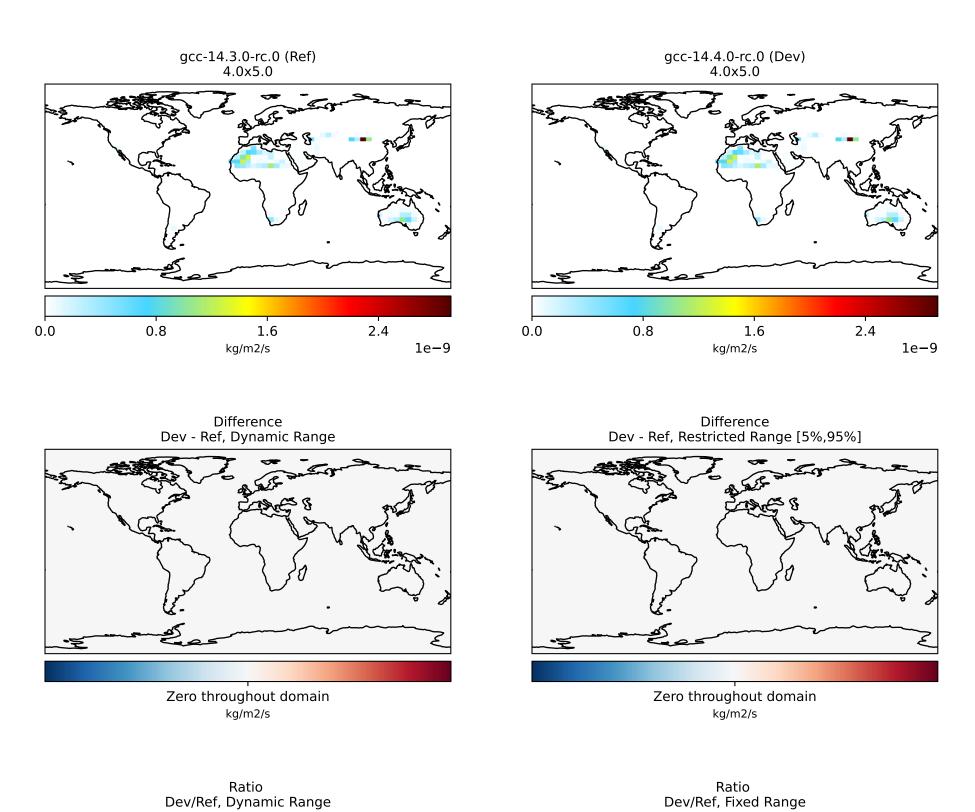


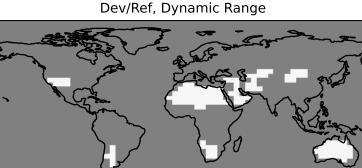
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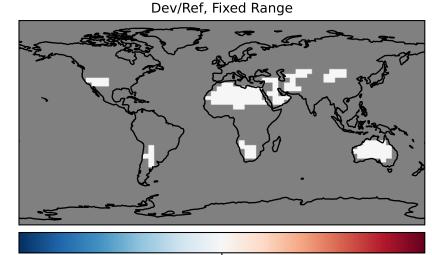
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EmisDST3_Natural (Oct2019)



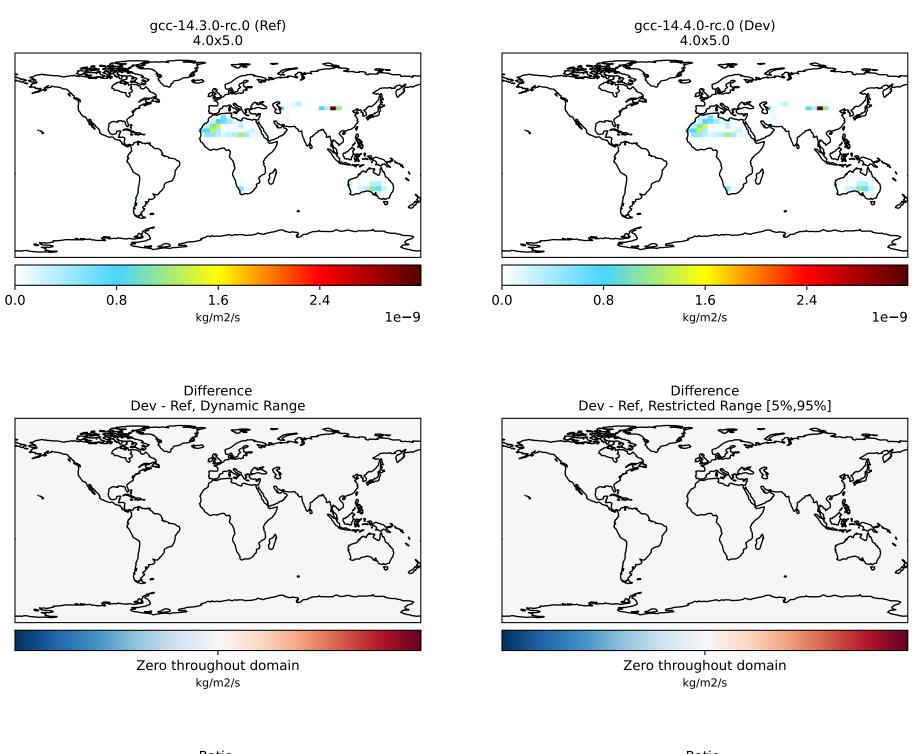


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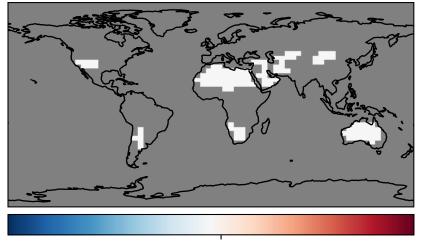


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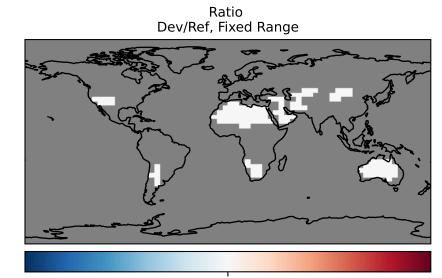
EmisDST4_Natural (Oct2019)



Ratio Dev/Ref, Dynamic Range

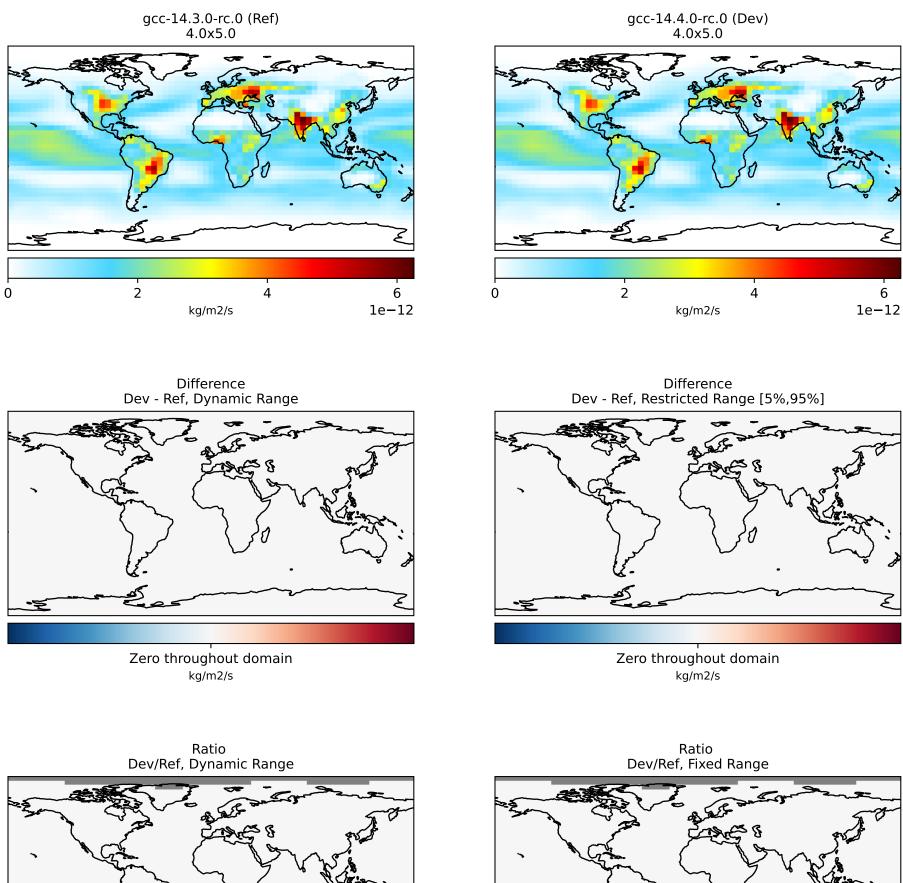


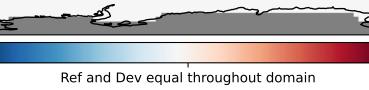
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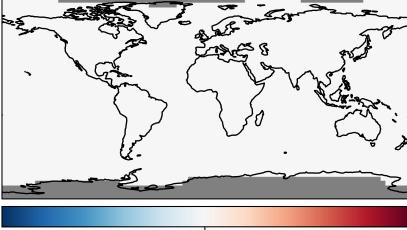
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EmisNH3_Natural (Oct2019)



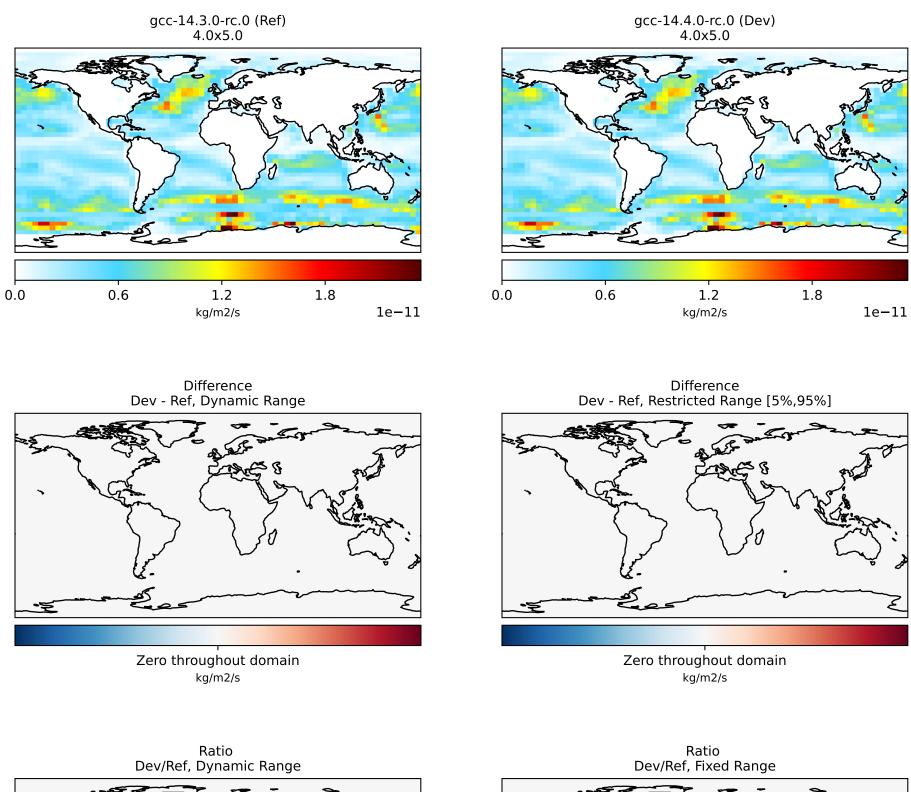


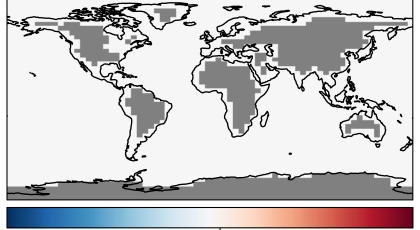
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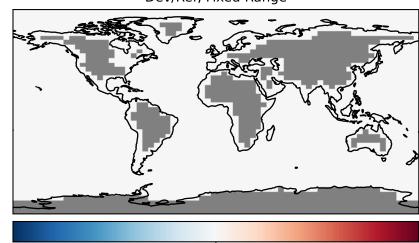
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EmisSALA_Natural (Oct2019)



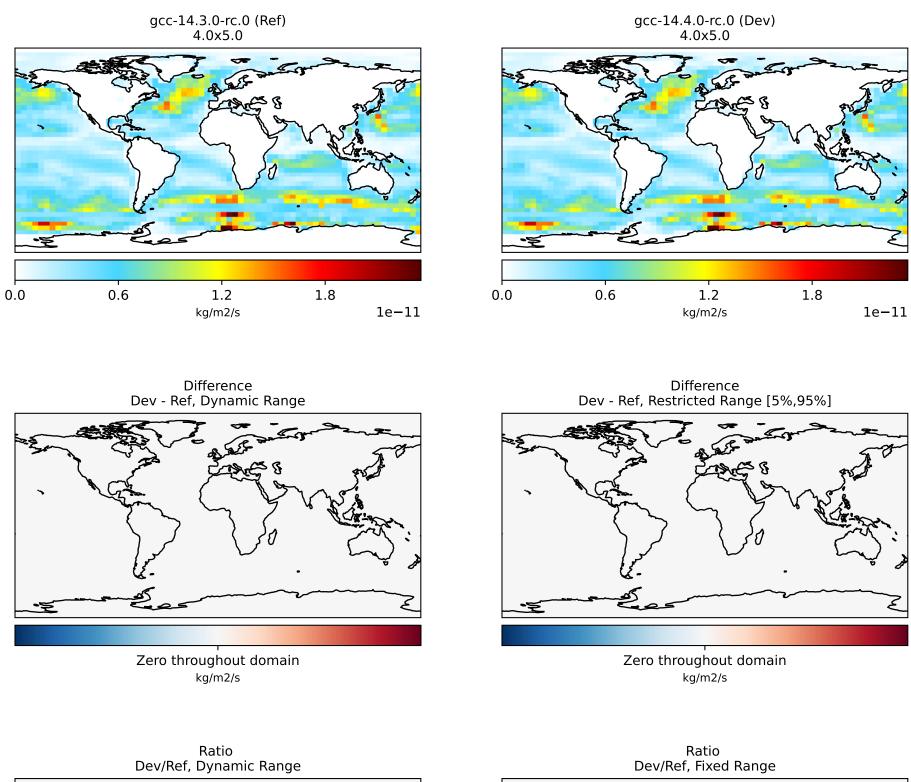


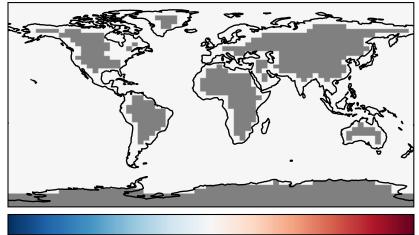
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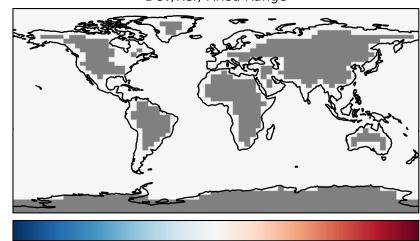
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EmisSALAAL_Natural (Oct2019)



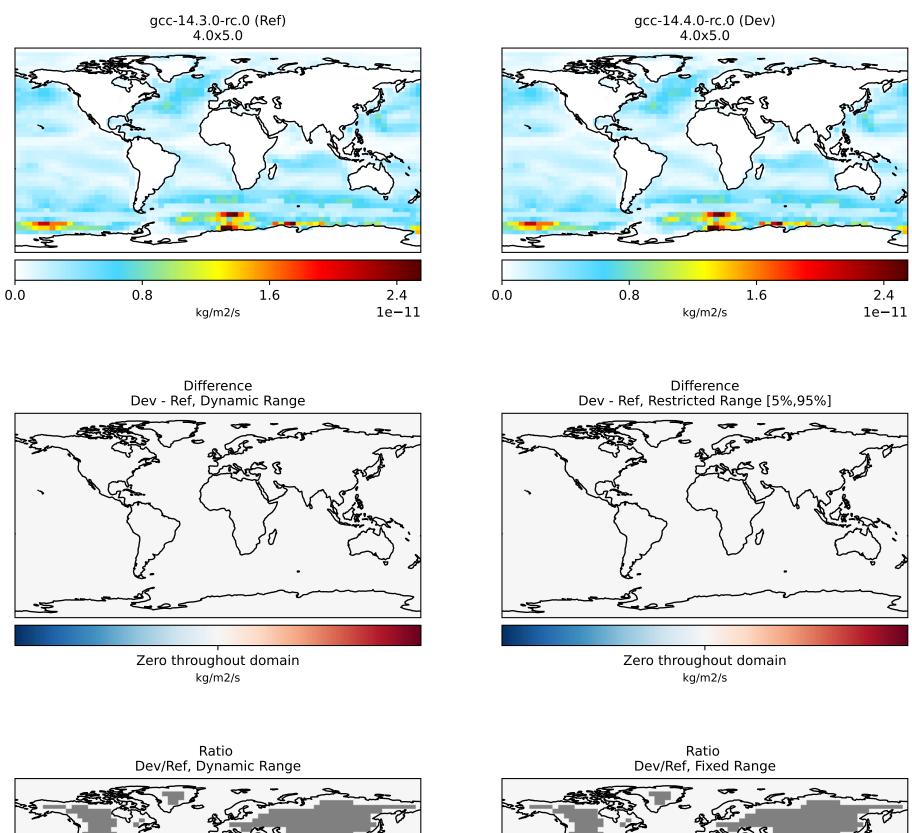


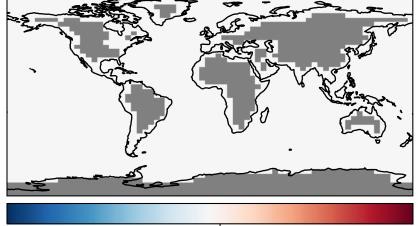
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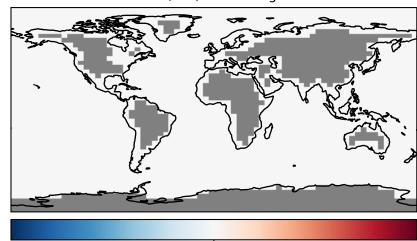
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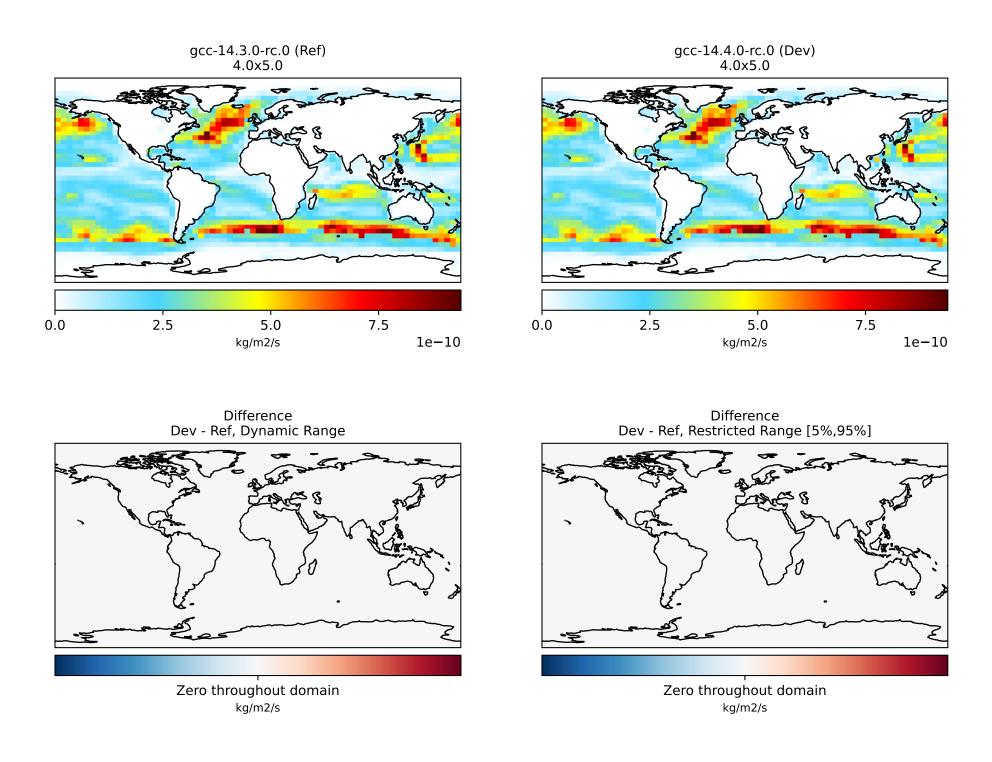


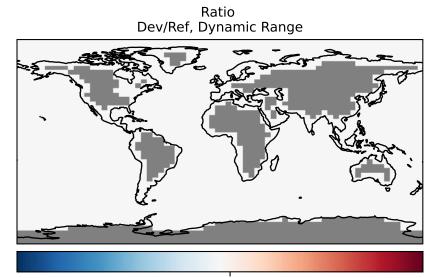
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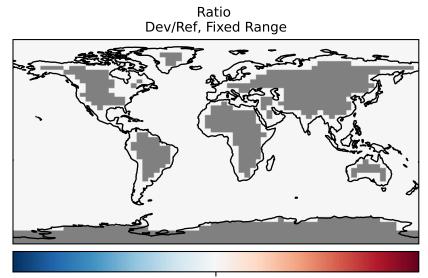
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EmisSALC_Natural (Oct2019)



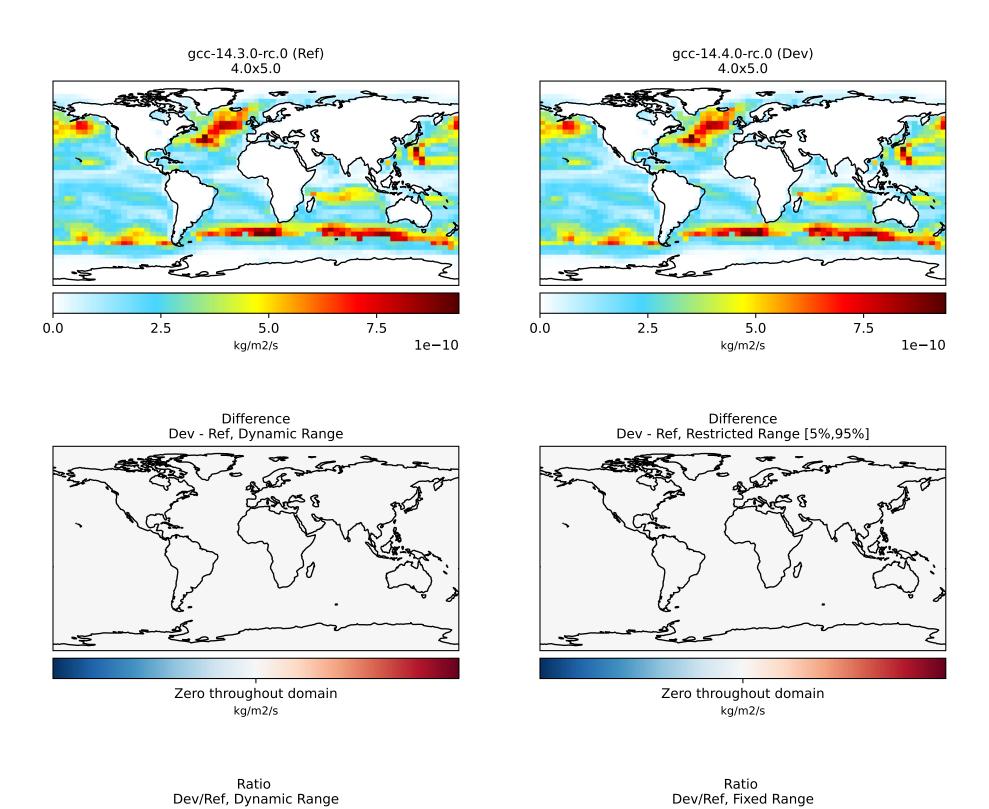


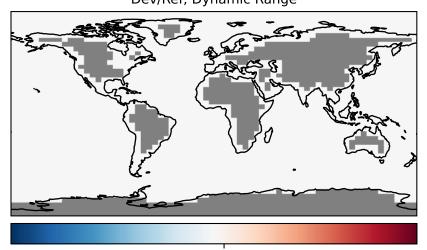
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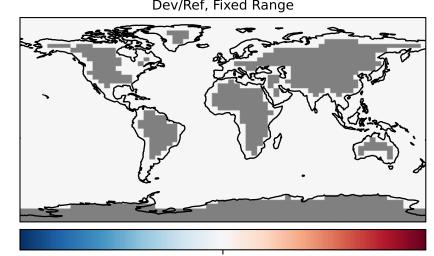
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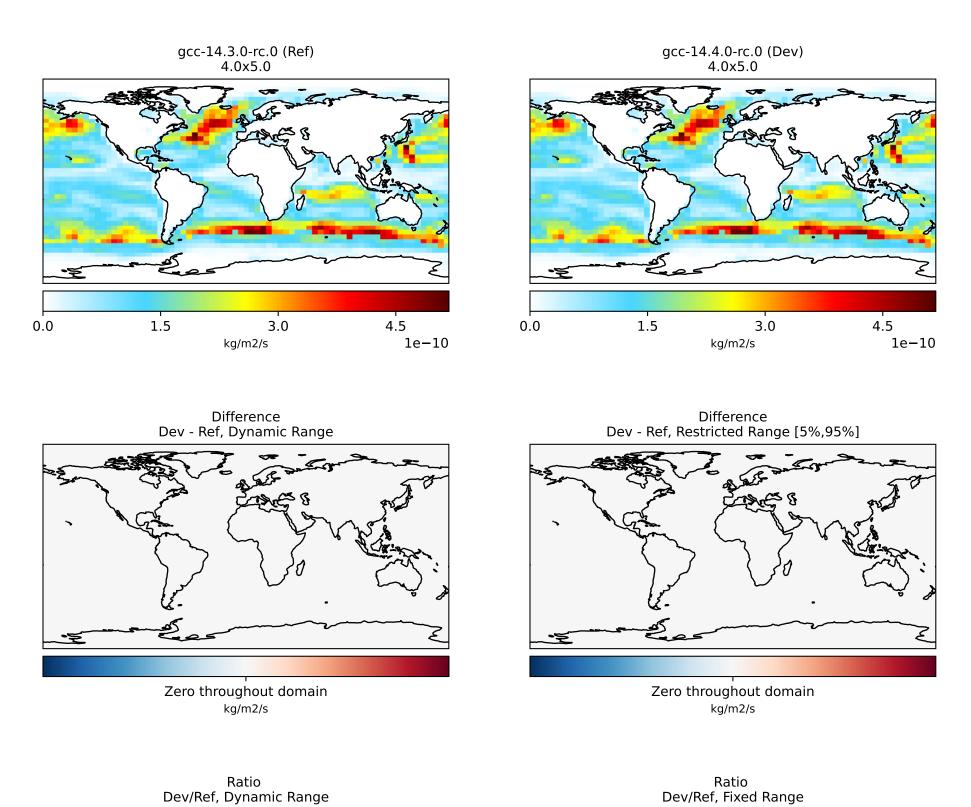


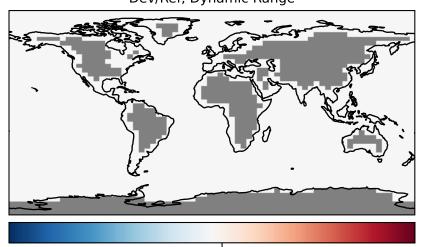
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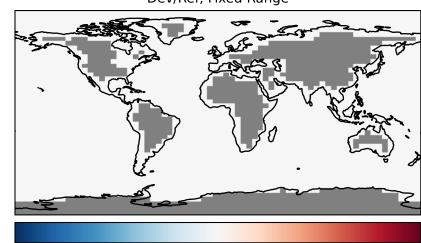
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EmisSALCCL_Natural (Oct2019)





Ref and Dev equal throughout domain unitless



Ref and Dev equal throughout domain unitless